

Digital Video Broadcasting (DVB)

Second generation framing structure, channel coding and modulation systems for Broadcasting, Interactive Services, News Gathering and other broadband satellite applications



European Telecommunications Standards Institute

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Foreword

This European Standard (Telecommunication Series) has been produced by the Joint Technical Committee (JTC) of the European Broadcasting Union (EBU), Comité Européen de Normalisation ELECTrotechnique (CENELEC) and the European Telecommunications Standards Institute (ETSI). The work of the JTC was based on the studies carried out by the European DVB Project under the auspices of the Ad Hoc Group on DVB-S2 of the DVB Technical Module. This joint group of industry, operators and broadcasters provided the necessary information on all relevant technical matters (see bibliography).

NOTE: The EBU/ETSI JTC was established in 1990 to co-ordinate the drafting of standards in the specific field of broadcasting and related fields. Since 1995 the JTC became a tripartite body by including in the Memorandum of Understanding also CENELEC, which is responsible for the standardization of radio and television receivers. The EBU is a professional association of broadcasting organisations whose work includes the co-ordination of its members' activities in the technical, legal, programme-making and programme-exchange domains. The EBU has Active Members in about 60 countries in the European Broadcasting Area; its headquarters is in Geneva*.

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Digital Video Broadcasting (DVB) Project

Founded in September 1993, the DVB Project is a market-led consortium of public and private sector organisations in the television industry. Its aim is to establish the framework for the introduction of MPEG-2 based digital television services. Now comprising over 200 organisations from more than 25 countries around the world, DVB fosters market-led systems, which meet the real needs, and economic circumstances, of the consumer electronics and the broadcast industry.

1 Scope

DVB-S (EN 300 421(bibliography)) was introduced as a standard in 1994 and DVB-DSNG (EN 301210(bibliography)) in 1997. The DVB-S standard specifies QPSK modulation and concatenated convolutional and Reed-Solomon channel coding, and is now used by most satellite operators worldwide for television and data broadcasting services. DVB-DSNG specifies, in addition to DVB-S format, the use of 8-PSK and 16-QAM modulation for satellite news gathering and contribution services.

Since 1997, digital satellite transmission technology has evolved somewhat:

- New channel coding schemes, combined with higher order modulation, promise more powerful alternatives to the DVB-S / DVB-DSNG coding and modulation schemes. The result is a capacity gain in the order of 30% at a given transponder bandwidth and transmitted EIRP, depending on the modulation type and code rate.
- Variable Coding and Modulation (VCM) may be applied to provide different levels of error protection to different service components (e.g. SDTV and HDTV, audio, multimedia)
- In the case of interactive and point-to-point applications, the VCM functionality may be combined with the use of return channels, to achieve Adaptive Coding and Modulation (ACM). This technique provides more exact channel protection and dynamic link adaptation to propagation conditions, targeting each individual receiving terminal. ACM systems promise satellite capacity gains of up to 100%-200%. In addition, service availability may be extended compared to a constant protection system (CCM) such as DVB-S or DVB-DSNG. Such gains are achieved by informing the satellite up-link station of the channel condition (e.g. C/N+1) of each receiving terminal via the satellite or terrestrial return channels.
- DVB-S and DVB-DSNG are strictly focused on a unique data format, the MPEG Transport Stream (ISO/IEC 13818-1 or a reference to it). Extended flexibility to cope with other input data formats (such as multiple Transport Streams, or generic data formats) is now possible without significant complexity increase.

This EN defines a “second generation” modulation and channel coding system (denoted the "System" or “DVB-S2” for the purposes of the present document) to make use of the improvements listed above. DVB-S2 is a single, very flexible standard, covering a variety of applications by satellite, as described below. It is characterised by:

- a flexible input stream adapter, suitable for operation with single and multiple input streams of various formats (packetised or continuous);
- a powerful FEC system based on LDPC (Low-Density Parity Check) codes concatenated with BCH codes, allowing Quasi-Error-Free operation at about 0.7 to 1 dB from the Shannon limit, depending on the transmission mode (AWGN channel, modulation constrained Shannon limit);
- a wide range of code rates (from 1/2 up to 9/10); 5 constellations, ranging in spectrum efficiency from 1 to 5 bit/second/Hz, optimised for operation over non-linear transponders;
- a set of three spectrum shapes with roll-off factors 0.35, 0.25 and 0.20;
- Adaptive Coding and Modulation (ACM) functionality, optimising channel coding and modulation on a frame-by-frame basis.

The System has been optimised for the following **broadband satellite applications**:

Broadcast Services (BS) digital multi-programme Television (TV) / High Definition Television (HDTV) broadcasting services

to be used for primary and secondary distribution in the Fixed Satellite Service (FSS) and the Broadcast Satellite Service (BSS) bands. DVB-S2 is intended to provide Direct-To-Home (DTH) services for consumer Integrated Receiver Decoder (IRD), as well as collective antenna systems (Satellite Master Antenna Television (SMATV)) and cable television head-end stations (possibly with remodulation, see EN 300 429 (bibliography)). DVB-S2 may be considered a successor to the current DVB-S standard EN 300421, and may be introduced for new services and allow for a long-term migration. BS services are transported in MPEG Transport Stream format. VCM may be applied on multiple transport stream to achieve a differentiated error protection for different services (TV, DHTV, audio, multimedia). To facilitate the reception of DVB-S services by DVB-S2 receivers, implementation of DVB-S in DVB-S2 chips is highly recommended.

Interactive Services (IS) Interactive data services including Internet access.

DVB-S2 is intended to provide interactive services to consumer IRDs and to personal computers, where DVB-S2's forward path supersedes the current DVB-S standard EN 300421 for interactive systems. The return path can be implemented using various DVB interactive systems, such as DVB-RCS (EN 301 790), DVB-RCP (ETS 300801), DVB-RCG (EN 301195), DVB-RCC (ES 200800). Data services are transported in (single or multiple) Transport Stream format according to EN 301 192 (e.g. using Multiprotocol Encapsulation), or in (single or multiple) generic stream format. DVB-S2 can provide Constant Coding and Modulation (CCM), or Adaptive Coding and Modulation (ACM), where each individual satellite receiving station controls the protection mode of the traffic addressed to it. Input Stream Adaptation for ACM is specified in **Annex D** of this EN.

Digital TV Contribution and Satellite News Gathering (DTVCD/DSNG)

Digital television contribution applications by satellite consist of point-to-point or point-to-multipoint transmissions, connecting fixed or transportable uplink and receiving stations. They are not intended for reception by the general public. According to ITU-R SNG.770-1 Recommendation, SNG is defined as "Temporary and occasional transmission with short notice of television or sound for broadcasting purposes, using highly portable or transportable uplink earth stations ...". Services are transported in single (or multiple) MPEG Transport Stream format. DVB-S2 can provide Constant Coding and Modulation (CCM), or Adaptive Coding and Modulation (ACM). In this latter case, a single satellite receiving station typically controls the protection mode of the full multiplex. Input Stream Adaptation for ACM is specified in **Annex D**.

Data content distribution/trunking and other professional applications (PS)

These services are mainly point-to-point or point-to-multipoint, including interactive services to professional head-ends, which re-distribute services over other media. Services may be transported in (single or multiple) generic stream format. The system can provide Constant Coding and Modulation (CCM), Variable Coding and Modulation (VCM) or Adaptive Coding and Modulation (ACM). In this latter case, a

single satellite receiving station typically controls the protection mode of the full TDM multiplex, or multiple receiving stations control the protection mode of the traffic addressed to each one. In either case, interactive or non-interactive, this document is only concerned with the forward broadband channel.

DVB-S2 is suitable for use on different satellite transponder bandwidths and frequency bands. The symbol rate is matched to given transponder characteristics, and, in the case of multiple carriers per transponder (FDM), to the frequency plan adopted. Examples of possible DVB-S2 use of are given in **Annex G**.

Digital transmissions via satellite are affected by power and bandwidth limitations. Therefore DVB-S2 provides for many transmission modes (FEC coding and modulations), giving different trade-offs between power and spectrum efficiency (see **Annex G**). For some specific applications (e.g. broadcasting) modes such as QPSK and 8PSK, with their quasi-constant envelope, are appropriate for operation with saturated satellite power amplifiers (in single carrier per transponder configuration). When higher power margins are available, spectrum efficiency can be further increased to reduce bit delivery cost. In these cases also 16APSK and 32APSK can operate in single carrier mode close to the satellite HPA saturation by pre-distortion techniques. All the modes are appropriate for operation in quasi-linear satellite channels, in multi-carrier Frequency Division Multiplex (FDM) type applications.

DVB-S2 is compatible with Moving Pictures Experts Group (MPEG-2 and MPEG-4) coded TV services (see ISO/IEC 13818-1 [1]), with a Transport Stream packet multiplex. Multiplex flexibility allows the use of the transmission capacity for a variety of TV service configurations, including sound and data services. All service components are Time Division Multiplexed (TDM) on a single digital carrier.

This EN:

- gives a general description of the DVB-S2 system;
- specifies the digitally modulated signal in order to allow compatibility between pieces of equipment developed by different manufacturers. This is achieved by describing in detail the signal processing principles at the modulator side, while the processing at the receive side is left open to different implementation solutions. However, it is necessary in this EN to refer to certain aspects of reception;
- identifies the global performance requirements and features of the System, in order to meet the service quality targets.

2 Normative references

References may be made to:

- a) specific versions of publications (identified by date of publication, edition number, version number, etc.), in which case, subsequent revisions to the referenced document do not apply; or
- b) all versions up to and including the identified version (identified by "up to and including" before the version identify); or
- c) all versions subsequent to and including the identified version (identified by "onwards" following the version identify); or
- d) publications without mention of a specific version, in which case the latest version applies.

A non-specific reference to an ETS shall also be taken to refer to the later versions published as an EN with the same number.

- [1] ISO/IEC 13818-1 and ISO/IEC 13818-2:: "Coding of moving pictures and associated audio".

- [2] ETSI: EN 300 421 V1.1.2 (1997-08) "Digital Video Broadcasting (DVB); Framing structure, channel coding and modulation for 11/12 GHz satellite services
- [3] ETSI: EN 301 210 "Digital Video Broadcasting (DVB); Framing structure, channel coding and modulation for Digital Satellite News Gathering and other professional applications by satellite"
- [4] CENELEC: EN 50083-9 "Interfaces for CATV/SMATV Headends and similar Professional Equipment"
- [5] ETSI, ETR..... "Digital Video Broadcasting (DVB): Implementation guidelines for the use of MPEG-2 Systems, Video and Audio for contribution applications"
- [6] ETSI, ETR..... "Digital Video Broadcasting (DVB): Implementation guidelines for the use of MPEG-4 Systems, Video and Audio."
- [7] EN 300 468 "Specification for Service Information (SI) in DVB systems"
- [8] ETSI, TBR 30 (December 1997): "Satellite Earth Stations and Systems (SES); Satellite News Gathering Transportable Earth Stations (SNG TES) operating in the 11-12/13-14 GHz frequency bands
- [9] ETSI, ETS 300 327 (September 1994): "Satellite Earth Stations and Systems (SES); Satellite News Gathering (SNG) Transportable Earth Stations (TESs) (13-14/11-12 GHz)"
- [10] ETSI, ETS 300 673 (March 1997): "Radio Equipment and Systems (RES); ElectroMagnetic Compatibility (EMC) standard for 4/6 GHz and 11/12/14 GHz Very Small Aperture Terminal (VSAT) equipment and 11/12/13/14 GHz Satellite News Gathering (SNG) Transportable Earth Station (TES) equipment"
- [11] ETSI: EN301 192 "Digital Video Broadcasting (DVB): " Specification for data broadcasting"

3 Symbols and abbreviations

3.1 Symbols

For the purposes of this EN, the following symbols apply:

α	Roll-off factor
γ	Ratio between constellation radii for 16APSK and 32APSK
C/N	Carrier-to-noise power ratio (N measured in a bandwidth equal to symbol rate)
C/N+I	Carrier-to-(Noise+Interference) ratio
d_{\min}	code minimum distance
E_b/N_0	Ratio between the energy per information bit and single sided noise power spectral density
E_s/N_0	Ratio between the energy per transmitted symbol and single sided noise power spectral density
f_N	Nyquist frequency
I, Q	In-phase, Quadrature phase components of the modulated signal
K_{bch}	number of bits of BCH uncoded Block
N_{bch}	number of bits of BCH coded Block
k_{ldpc}	number of bits of LDPC uncoded Block
n_{ldpc}	number of bits of LDPC coded Block
η_{mod}	number of transmitted bits per constellation symbol
M	number of modulated symbols in SLOT
P	number of pilot symbols in a pilot block
r_m	In-band ripple (dB)
R_s	Symbol rate corresponding to the bilateral Nyquist bandwidth of the modulated

	signal
R_u	Useful bit rate at the DVB-S2 system input
S	Number of Slots in a XFECFRAME
T_s	Symbol period

3.2 Abbreviations

For the purposes of this EN, the following abbreviations apply:

ACM	Adaptive Coding and Modulation
AWGN	Additive White Gaussian Noise
BB	Baseband
BER	Bit Error Ratio
BCH	BCH Multiple error correction binary block code
BPSK	Binary Phase Shift Keying
BS	Broadcast Service
B_s	Bandwidth of the frequency Slot allocated to a service
BSS	Broadcast Satellite Service
BW	Bandwidth (at -3 dB) of the transponder
CCM	Constant Coding and Modulation
D	Decimal Notation
DSNG	Digital Satellite News Gathering
DTH	Direct To Home
DVB	Digital Video Broadcasting Project
DVB-S2	the DVB-S2 system as specified in this EN
EBU	European Broadcasting Union
EN	European Norm
ETS	European Telecommunication Standard
FDM	Frequency Division Multiplex
FEC	Forward Error Correction
FSS	Fixed Satellite Service
HEX	Hexadecimal notation
HDTV	High Definition Television
IBO	Input Back Off
IF	Intermediate Frequency
IMUX	Input Multiplexer - Filter
IRD	Integrated Receiver Decoder
IS	Interactive Services
ITU	International Telecommunications Union
LDPC	Low Density Parity Check (codes)
LSB	Least Significant Bit
MPEG	Moving Pictures Experts Group
MSB	Most Significant Bit: in DVB-S2 the MSB is always transmitted first
MUX	Multiplex
OBO	Output Back Off
OCT	Octal notation
OMUX	Output Multiplexer - Filter
PER	(MPEG TS) Packet Error Rate
PL	Physical Layer
PS	Professional Services
PSK	Phase Shift Keying
PRBS	Pseudo Random Binary Sequence
QEF	Quasi-Error-Free
QPSK	Quaternary Phase Shift Keying
RF	Radio Frequency
SNG	Satellite News Gathering
SMATV	Satellite Master Antenna Television
TBD	To Be Defined
TDM	Time Division Multiplex
TV	Television
TWTA	Travelling Wave Tube Amplifier
VCM	Variable Coding and Modulation

16APSK	16-ary Amplitude and Phase Shift Keying
32APSK	32-ary Amplitude and Phase Shift Keying
8PSK	8-ary Phase Shift Keying

4 Transmission system description

4.1 System definition

The System is defined as the functional block of equipment performing the adaptation of the baseband digital signals, from the output of a single (or multiple) MPEG transport stream multiplexer(s) (see ISO/IEC 13818-1 [1]), or from the output of a single (or multiple) generic data source(s), to the satellite channel characteristics. The System is designed to support source coding as defined in [1], [5], [6]. Data services may be transported in Transport Stream format according to EN 301 192 (e.g., using Multi-protocol Encapsulation), or Generic Stream format.

If the received signal is above the C/N+I threshold, the Forward Error Correction (FEC) technique adopted in the System is designed to provide a "Quasi Error Free" (QEF) quality target. The definition of QEF adopted for DVB-S2 is "less than one uncorrected error-event per transmission hour at the level of a 5 Mbit/s single TV service decoder", corresponding to a Transport Stream Packet Error Ratio PER 10^{-7} before de-multiplexer.

4.2 System architecture

According to **Figure 1**, the DVB-S2 System shall be composed of a sequence of functional blocks as described below.

Mode Adaptation shall be application dependent. It shall provide input stream interfacing, Input Stream Synchronisation (optional), null-packet deletion (for ACM and Transport Stream input format only), CRC-8 coding for error detection at packet level in the receiver (for packetised input streams only), merging of input streams (for Multiple Input Stream modes only) and slicing into DATA FIELDS. For Constant Coding and Modulation (CCM) and single input Transport Stream, Mode Adaptation shall consist of a "transparent" DVB-ASI (or DVB-parallel) to logical-bit conversion and CRC-8 coding. For Adaptive Coding and Modulation (ACM), Mode Adaptation shall be according to **Annex D**.

A Base-Band Header shall be appended in front of the Data Field, to notify the receiver of the input stream format and Mode Adaptation type. To be noted that the MPEG multiplex transport packets may be asynchronously mapped to the Base-Band Frames.

Stream Adaptation shall be applied, to provide Padding to complete a Base-Band Frame and Base-Band Scrambling.

Forward Error Correction (FEC) Encoding shall be carried out by the concatenation of BCH outer codes and LDPC (Low Density Parity Check) inner codes (rates 1/2, 3/5, 2/3, 3/4, 4/5, 5/6, 8/9, 9/10). Depending on the application area, the FEC coded block shall have length $n_{ldpc}=64800$ bits or 16200 bits. When VCM and ACM is used, FEC and modulation mode may be changed in different frames, but remains constant within a frame. Bit interleaving shall be applied to FEC coded bits for 8PSK, 16APSK and 32APSK.

Mapping into BPSK, QPSK, 8PSK, 16APSK and 32APSK constellations shall be applied, depending on the application area. Gray mapping of constellations shall be used for BPSK, QPSK and 8PSK.

Physical Layer Framing shall be applied, synchronous with the FEC frames, to provide Dummy PLFRAME insertion, Physical Layer (PL) Signalling, pilot symbols insertion (optional) and Physical Layer Scrambling for energy dispersal. Dummy PLFRAMEs are transmitted when no useful data is ready to be sent on the channel. The System provides a regular physical layer framing structure, based on SLOTS of M=90 modulated symbols, allowing reliable receiver synchronisation on the FEC block structure. A slot is devoted to physical layer signalling, including Start-of-Frame delimitation and transmission mode definition. This mechanism is suitable also for VCM and ACM demodulator

setting. Carrier recovery in the receivers may be facilitated by the introduction of a regular raster of pilot symbols (P=36 pilot symbols every 16 SLOTS of 90 symbols), while a pilot-less transmission mode is also available, offering an additional 2.4% useful capacity.

Base-Band Filtering and Quadrature Modulation shall be applied, to shape the signal spectrum (squared-root raised cosine, roll-off factors 0.35 or 0.25 or 0.20) and to generate the RF signal.

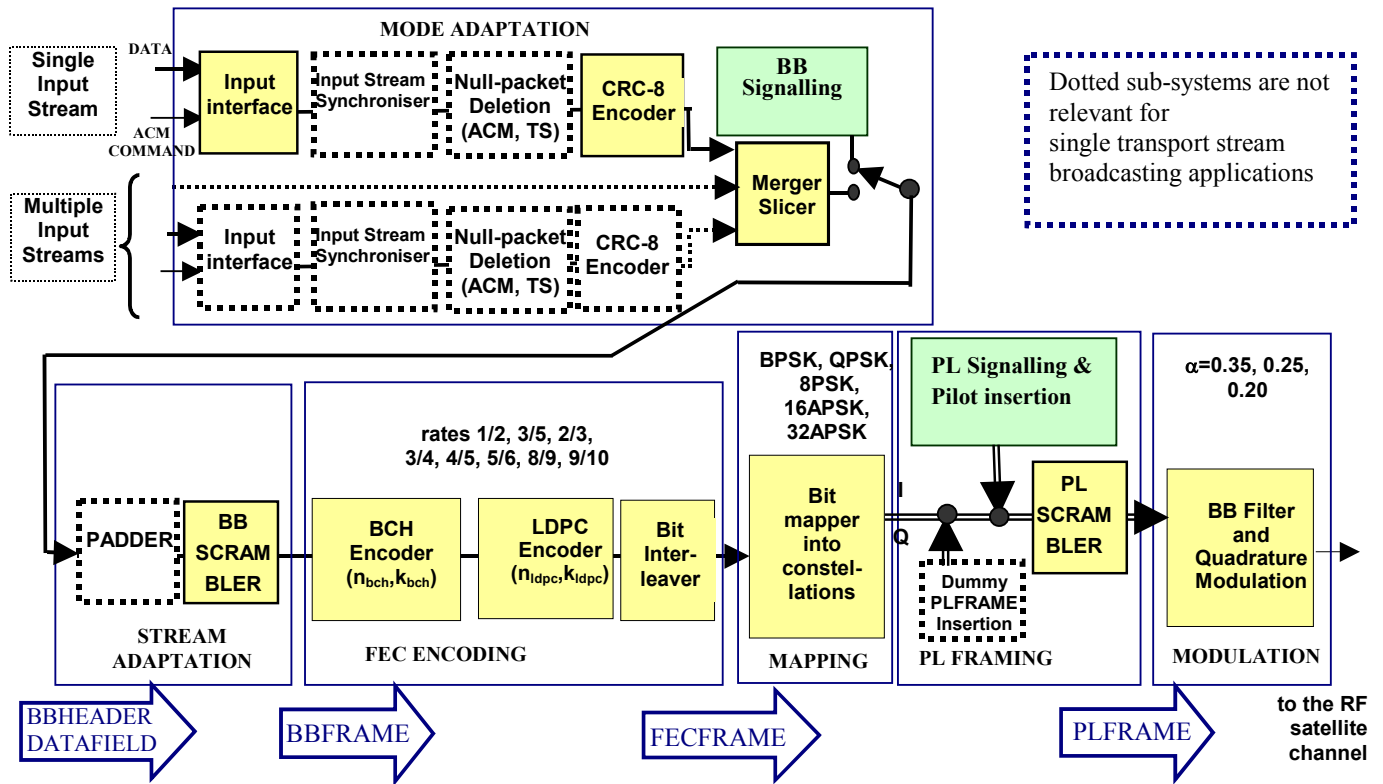


Figure 1: Functional block diagram of the DVB-S2 System

4.3 System configurations

The following Table 1 associates the System configurations to the applications areas. Guidelines for mode selection are given in Annex G.

System configurations		Broadcast Services	Interactive Services	DSNG	Professional Services
BPSK	1/2, 3/5, 2/3	O	N	N	N
QPSK	1/2, 3/5, 2/3, 3/4, 4/5, 5/6, 8/9, 9/10	N	N	N	N
8PSK	3/5, 2/3, 3/4, 5/6, 8/9, 9/10	N	N	N	N
16APSK	2/3, 3/4, 4/5, 5/6, 8/9, 9/10	O	N	N	N
32APSK	3/4, 4/5, 5/6, 8/9	O	N	N	N
CCM		N	N	N	N
VCM		O	O	O	O
ACM		NA	N	O	O

FECFRAME (normal)	64800 (bits)	N	N	N	N
FECFRAME (short)	16200 (bits)	NA	N	O	N
Single Transport Stream		N	N*	N	N
Multiple Transport Streams		O	O*	O	O
Single Generic Stream		NA	O*	NA	O
Multiple Generic Streams		NA	O*	NA	O
Roll-off 0.35, 0.25 and 0.20		N	N	N	N
Input Stream Synchroniser		NA***	O**	O**	O**
Null Packet Deletion		NA	O**	O**	O**
Dummy Frame insertion		NA***	N	N	N

Within this EN, a number of configurations and mechanisms are defined as "Optional". Configurations and mechanisms explicitly indicated as "optional" within this EN, for a given application area, need not be implemented in the equipment to comply with this EN. Nevertheless, when an "optional" mode or mechanism is implemented, it shall comply with the specification as given in this EN.

5 Subsystems specification

The subsystem specification description is organised according to the functional block diagram of **Figure 1**.

5.1 Mode Adaptation

This sub-system shall perform Input Interfacing, Input Stream Synchronisation (optional), Null-packet deletion (for TS input streams and ACM only), CRC-8 encoding for error detection (for packetised input streams only), input stream merging (for multiple input streams only) and input stream slicing in DATA FIELDS. Finally, base-band signalling shall be inserted, to notify the receiver of the adopted Mode Adaptation format.

According to **Figure 3**, the input sequence(s) is (are):

- Single or multiple Transport Streams (TS)
- Single or multiple Generic Streams (packetised or continuous)

The output sequence is a BBHEADER (80 bits) followed by a DATA FIELD.

5.1.1 Input interface

The System, as defined in this EN, shall be delimited by the interfaces given in **Table 2**.

Table 2: System interfaces

Location	Interface	Interface type	Connection	Multiplicity
Transmit station	Input	MPEG [1, 4] Transport Stream (Note 1)	from MPEG multiplexer	Single or multiple
Transmit station	Input (NOTE 2)	Generic Stream	From data sources	Single or multiple
Transmit station	Input (NOTE 3)	ACM command	From rate control unit	Single
Transmit station	Output	70/140 MHz IF, L-band IF, RF (Note 4)	to RF devices	Single or multiple

NOTE 1: For interoperability reasons, the asynchronous serial interface (ASI) with 188 bytes format, data burst mode (bytes regularly spread over time) is recommended.

NOTE 2: For data services

Note 3: For ACM only. Allows external setting of the ACM transmission mode

Note 3: IF shall be higher than twice the symbol rate

The input interface subsystem shall map the input electrical format into internal logical-bit format. The first received bit will be indicated as the Most Significant Bit (MSB).

A Transport Stream shall be characterised by User Packets (UP) of constant length $UPL=188 \times 8$ bits (one MPEG packet), the first byte being a Sync-byte (47_{HEX}).

A Generic Stream shall be characterised by a continuous bit-stream or a stream of constant-length User Packets (UP), with length UPL bits (maximum UPL value 64 K, $UPL=0_D$ means continuous stream, see section 5.1.5). A variable length packet stream, or a constant length packet exceeding 64Kbit, shall be treated as a continuous stream.

For Generic packetised streams, if a synch-byte is the first byte of the UP, it shall be left unchanged, otherwise a synch-byte= 0_D shall be inserted before each packet, and UPL shall be increased by eight. UPL information may be derived by static modulator setting.

“ACM Command” signalling input shall allow setting, by an external “transmission mode control unit”, of the transmission parameters to be adopted by the DVB-S2 modulator, for a specific portion of input data. ACM command shall be according to **Annex D**.

5.1.2 Input Stream Synchroniser (optional, not relevant for single transport stream broadcasting)

Data processing in the DVB-S2 modulator may produce variable transmission delay on the user information. The Input Stream Synchroniser subsystem (optional) shall provide suitable means to guarantee constant-bit-rate (CBR) and constant end-to-end transmission delay for packetised input streams (e.g. for Transport Streams). This process shall follow the specification given in **Annex D**. Examples of receiver implementation are given in **Annex E**.

5.1.3 Null-Packet Deletion (ACM and Transport Stream only)

For ACM modes and Transport Stream input data format, MPEG null-packets shall be identified (PID=8191_D) and removed according to **Annex D**. This allows to reduce the information rate and increase the error protection in the modulator. The process is carried-out in a way that the removed null-packets can be re-inserted in the receiver in the exact place where they originally were. This process shall follow the specification given in **Annex D**.

5.1.4 CRC-8 encoder (for packetised streams only)

If UPL=0_D (continuous generic stream) this sub-system shall pass forward the input stream without modifications.

If UPL≠0_D the input stream is a sequence of User Packets of length UPL bits, preceded by a sync-byte (the sync-byte being = 0_D when the original stream did not contain a sync-byte).

The useful part of the UP (excluding the sync-byte) shall be processed by a systematic 8-bit CRC encoder. The generator polynomial shall be:

$$g(X) = (X^5 + X^4 + X^3 + X^2 + 1)(X^2 + X + 1)(X + 1) = X^8 + X^7 + X^6 + X^4 + X^2 + 1$$

The CRC encoder output shall be computed as:

$$\text{CRC} = \text{remainder} [X^8 u(X) : g(X)]$$

where $u(X)$ is the input sequence (UPL – 8 bits) to be systematically encoded. **Figure 2** gives a possible implementation of the CRC generator by means of a shift register (Note: the register shall be initialised to all zeros before the first bit of each sequence enters the circuit). The computed CRC-8 shall replace the sync-byte of the following UP. As described in **section 5.1.6**, the sync-byte is copied into the SYNC field of the BBHEADER for transmission.

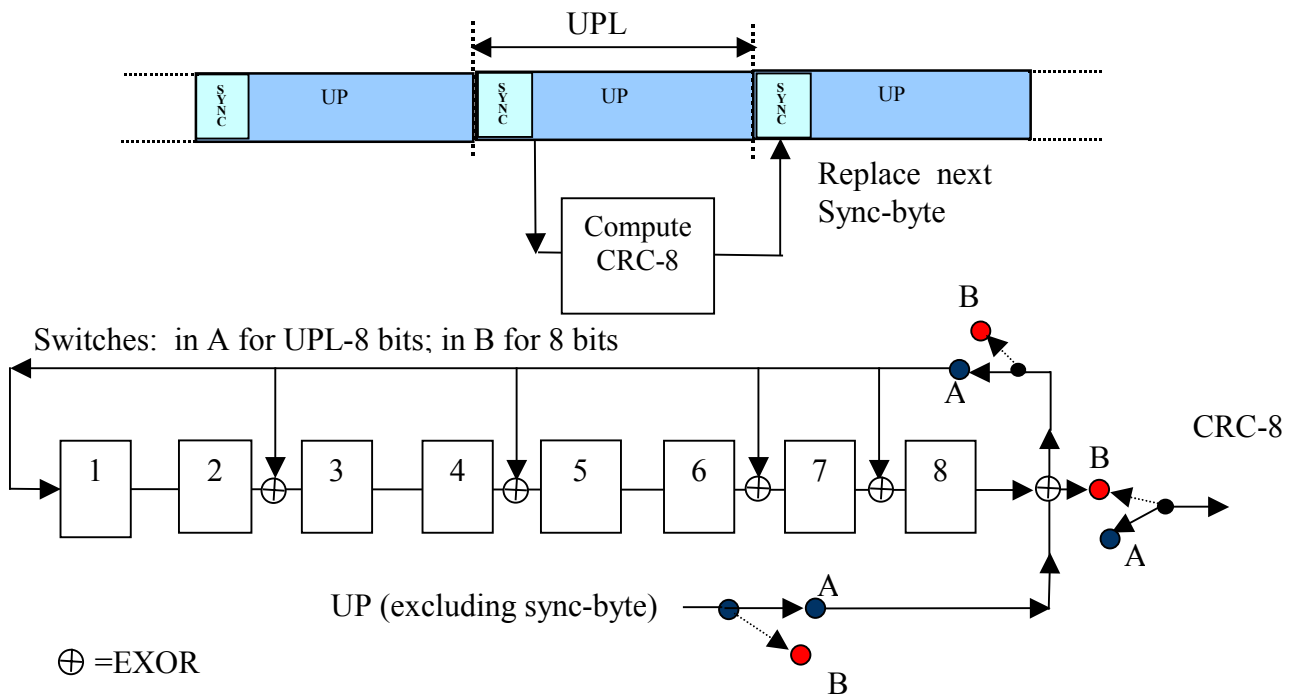


Figure 2: Implementation of the CRC-8 encoder

5.1.5 Merger / Slicer

According to **Figure 3**, the Merger/Slicer input stream(s) is (are) organised as Generic continuous Stream(s) or Packetised Input Stream(s). The UP length is UPL bits (where UPL=0 means continuous sequence). The input stream(s) shall be buffered until the Merger/Slicer may read them.

The Merger/Slicer shall read (i.e. slice) from its input (single input stream), or from one of its inputs (multiple input streams) a DATA FIELD, composed of DFL bits (Data Field Length), where:

$$k_{\text{BCH}} - (10 \times 8) \geq \text{DFL} \geq 0 \quad (k_{\text{BCH}} \text{ as per Table 5, 80 bits are dedicated to the BBHEADER, see 5.1.6})$$

A DATA FIELD shall be composed of bits taken from a single input port and shall be transmitted in a homogeneous transmission mode (FEC code and modulation). The Merger/Slicer prioritisation policies are application dependent and shall follow the strategies described in **Table 4** (Single Transport Stream Broadcast services) and in **Table D1** of **Annex D** (for other application areas).

Depending on the applications, the Merger/Slicer shall either allocate a number of input bits equal to the maximum DATAFIELD capacity ($\text{DFL} = k_{\text{BCH}} - 80$), thus breaking UPs in subsequent DATAFIELDS, or shall allocate an integer number of UPs within the DATAFIELD, making the DFL variable within the above specified boundaries.

When a DATA FIELD is not available at the merger/slicer request on any input port, the Physical Layer Framing sub-system shall generate and transmit a DUMMY PLFRAME (see **section 5.5.1** and **Table 12**).

After Sync-byte replacing by CRC-8 (see **section 5.1.4**), it is necessary to provide the receiver a method to recover UP synchronisation (when the receiver is already synchronised to the DATA FIELD). Therefore the number of bits from the beginning of the DATA FIELD and the beginning of the first complete UP (first bit of the CRC-8) (see **Figure 3**) shall be detected by the Merger/Slicer and stored in SYNC D field (i.e. SYNC Distance) of the Base-Band Header (see **section 5.1.6**). For example, $\text{SYNCD} = 0_D$ means that the first USER PACKET is aligned to the DATA FIELD.

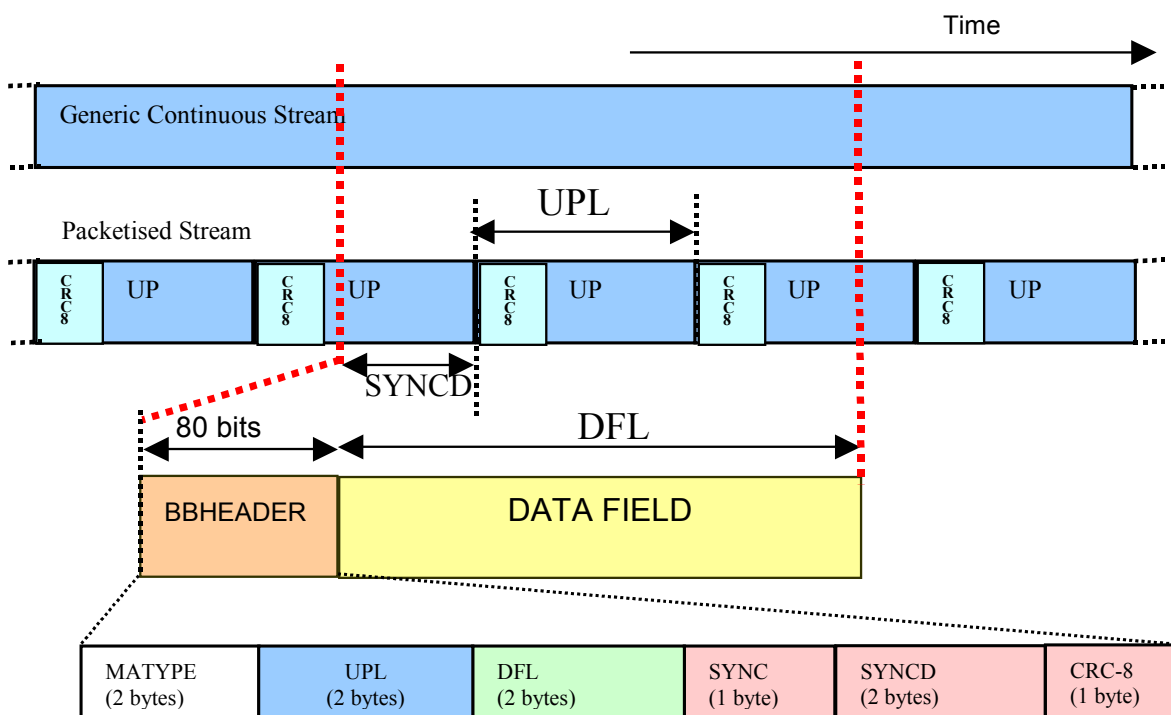


Figure 3: Stream format at the output of the MODE ADAPTER

5.1.6 Base-Band Header insertion

A fixed length base-band Header (BBHEADER) of 10 bytes shall be inserted in front of the DATA FIELD, describing its format (the maximum efficiency loss introduced by the BBHEADER is 0.25% for $n_{ldpc}=64800$ and 1% for $n_{ldpc}=16200$ assuming inner code rate 1/2):

MATYPE (2 bytes): describes the input stream(s) format, the type of Mode Adaptation and the transmission Roll-off factor, as explained in **Table 3**:

First byte (**MATYPE-1**):

- TS/GS field (2 bits): Transport Stream Input or Generic Stream Input (packetised or continuous)
- SIS/MIS field (1 bit): Single Input Stream or Multiple Input Stream
- CCM/ACM field (1 bit): Constant Coding and Modulation or Adaptive Coding and Modulation (VCM is signalled as ACM)
- ISSYI (1 bit), (Input Stream Synchronisation Indicator): If ISSYI=1= active, the ISSY field is inserted after UPs (see **Annex D**).
- NPD (1 bit): Null-packet deletion active/not active
- RO (2 bits): Transmission Roll-off factor (α)

Second byte (**MATYPE-2**):

- If SIS/MIS= Multiple Input Stream, then second byte=Input Stream Identifier (ISI); else second byte reserved

UPL (2 bytes): User Packet Length in bits, in the range [0,65535]. Examples:

- 0000_{HEX}= continuous stream
- 000A_{HEX}= UP length of 10 bits
- UPL=188x8_D for MPEG transport stream packets

DFL (2 bytes): Data Field Length in bits, in the range [0,58112]. Examples:

- 000A_{HEX}= Data Field length of 10 bits

SYNC (1 byte): copy of the User Packet Sync-byte. Examples:

- SYNC= 47_{HEX} for MPEG transport stream packets
- SYNC= 00_{HEX} when the input Generic packetised stream did not contain a sync-byte (therefore the receiver, after CRC-8 decoding, shall remove the CRC-8 field without reinserting the Sync-byte)
- SYNC= not relevant for Generic continuous input streams

SYNCD (2 bytes): distance in bits from the beginning of the DATA FIELD and the first complete UP (first bit of the CRC-8). SYNCD=65535_D means that no complete UD starts in the DATA FIELD.

CRC-8 (1 byte): error detection code applied to the first 9 bytes of the BBHEADER.

CRC-8 shall be computed using the encoding circuit of **Figure 2** (switch in A for 72 bits, in B for 8 bits).

The BBHEADER transmission order is from the MSB of the TS/GS field.

Table 4 shows the BBHEADER and the slicing policy for a Single Transport Stream Broadcast Service. For other application areas, BBHEADERS and slicing policies are defined in **Annex D, Table D1**.

TS/GS	SIS/MIS	CCM/ACM	ISSYI	NPD	RO
11=Transport	1=single	1=CCM	1=active	1=active	00=0.35
00=Generic Packetised	0=multiple	0=ACM	0=not-active	0=not-active	01=0.25
01=Generic continuous					10=0.20
10=reserved					11=reserved

Table 4: BBHEADER (Mode Adaptation characteristics) and Slicing Policy for Single Transport Stream Broadcast services								
Application area / configuration	MATYPE-1	MATYPE-2	UPL	DFL	SYNC	SYNCD	CRC-8	Slicing policy
Broadcasting services / CCM, single TS	11-1-1-0-0-Y	XXXXXXXX	188 _D x8	k _{BCH} – 80 _D	47 _{HEX}	Y	Y	Break No timeout No Padding No Dummy frame
X= not defined; Y=according to configuration/computation Break= break packets in subsequent DATAFIELDS; Timeout: maximum delay in merger/slicer buffer								

5.2 Stream Adaptation

Stream Adaptation (see **Figures 1** and **4**) provides padding to complete a constant length (K_{bch} bits) BBFRAME and scrambling. K_{bch} depends on the FEC rate, as reported in **Tables 5**. Padding may be applied in circumstances when the user data available for transmission are not sufficient to completely fill a BBFRAME, or when an integer number of UPs has to be allocated in a BBFRAME. The input stream shall be a BBHEADER followed by a DATA FIELD. The output stream shall be a BBFRAME.

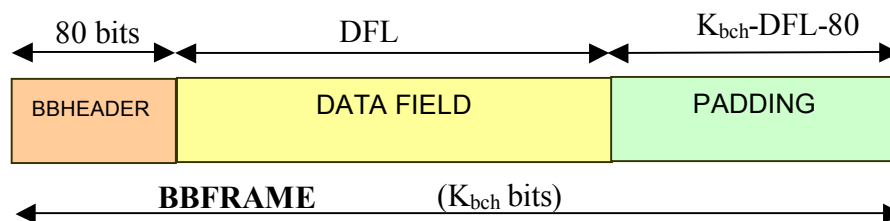


Figure 4: BBFRAME format at the output of the STREAM ADAPTER

5.2.1 Padding

(K_{bch} -DFL-80) zero bits shall be appended after the DATA FIELD. The resulting BBFRAME shall have a constant length of K_{bch} bits. For Broadcast Service applications, DFL= K_{bch} -80, therefore no padding shall be applied.

5.2.2 BB Scrambling

The complete BBFRAME shall be randomised. The randomisation sequence shall be synchronous with the BBFRAME, starting from the MSB and ending after K_{bch} bits.

The scrambling sequence shall be generated by the feed-back shift register of **Figure 5**. The polynomial for the Pseudo Random Binary Sequence (PRBS) generator shall be:

$$1 + X^{14} + X^{15}$$

Loading of the sequence (100101010000000) into the PRBS register, as indicated in **Figure 5**, shall be initiated at the start of every BBFRAME.

For short FECFRAMEs, some of the codes shall be shortened versions of longer (k_{ldpcm}, n_{ldpcm}) mother codes of block size $n_{ldpcm} > 16200$.

LDPC Mother Code Rate k_{ldpcm}/n_{ldpcm}	BCH Uncoded Block K_{bch}	BCH coded block N_{bch} LDPC Uncoded Block k_{ldpc}	BCH t-error correction	k_{ldpcm}	n_{ldpcm}	Effective LDPC Rate $k_{ldpc}/16200$	LDPC Coded Block n_{ldpc}
1/2	7032	7200	12	9000	18000	0.444	16200
3/5	9552	9720	12	9720	16200	3/5	16200
2/3	10632	10800	12	10800	16200	2/3	16200
3/4	11712	11880	12	12960	17280	0.733	16200
4/5	12432	12600	12	14400	18000	0.777	16200
5/6	13152	13320	12	14400	17280	0.822	16200
8/9	14232	14400	12	14400	16200	8/9	16200

5.3.1 Outer encoding (BCH)

A t-error correcting BCH (N_{bch}, K_{bch}) code shall be applied to each BBFRAME (K_{bch}) to generate an error protected packet. The BCH code parameters for $n_{ldpc}=64800$ are given in **Table 5a** and for $n_{ldpc}=16200$ in **Table 5b**.

The generator polynomial of the t error correcting BCH encoder is obtained by multiplying the first t polynomials in **Table 6**.

$g_1(x)$	$1+x^2+x^3+x^5+x^{16}$
$g_2(x)$	$1+x+x^4+x^5+x^6+x^8+x^{16}$
$g_3(x)$	$1+x^2+x^3+x^4+x^5+x^7+x^8+x^9+x^{10}+x^{11}+x^{16}$
$g_4(x)$	$1+x^2+x^4+x^6+x^9+x^{11}+x^{12}+x^{14}+x^{16}$
$g_5(x)$	$1+x+x^2+x^3+x^5+x^8+x^9+x^{10}+x^{11}+x^{12}+x^{16}$
$g_6(x)$	$1+x^2+x^4+x^5+x^7+x^8+x^9+x^{10}+x^{12}+x^{13}+x^{14}+x^{15}+x^{16}$
$g_7(x)$	$1+x^2+x^5+x^6+x^8+x^9+x^{10}+x^{11}+x^{13}+x^{15}+x^{16}$
$g_8(x)$	$1+x+x^2+x^5+x^6+x^8+x^9+x^{12}+x^{13}+x^{14}+x^{16}$
$g_9(x)$	$1+x^5+x^7+x^9+x^{10}+x^{11}+x^{16}$
$g_{10}(x)$	$1+x+x^2+x^5+x^7+x^8+x^{10}+x^{12}+x^{13}+x^{14}+x^{16}$
$g_{11}(x)$	$1+x^2+x^3+x^5+x^9+x^{11}+x^{12}+x^{13}+x^{16}$
$g_{12}(x)$	$1+x+x^5+x^6+x^7+x^9+x^{11}+x^{12}+x^{16}$

BCH encoding of information bits $\mathbf{m} = (m_{k_{bch}-1}, m_{k_{bch}-2}, \dots, m_1, m_0)$ onto a codeword

$\mathbf{c} = (m_{k_{bch}-1}, m_{k_{bch}-2}, \dots, m_1, m_0, d_{n_{bch}-k_{bch}-1}, d_{n_{bch}-k_{bch}-2}, \dots, d_1, d_0)$ is achieved as follows:

- Multiply the message polynomial $m(x) = m_{k_{bch}-1}x^{k_{bch}-1} + m_{k_{bch}-2}x^{k_{bch}-2} + \dots + m_1x + m_0$ by $x^{n_{bch}-k_{bch}}$
- Divide $x^{n_{bch}-k_{bch}} m(x)$ by $g(x)$, the generator polynomial. Let $d(x) = d_{n_{bch}-k_{bch}-1}x^{n_{bch}-k_{bch}-1} + \dots + d_1x + d_0$ be the remainder.
- Set the codeword polynomial $c(x) = x^{n_{bch}-k_{bch}} m(x) + d(x)$

5.3.2 Inner encoding (LDPC)

LDPC encoder systematically encodes an information block of size k_{ldpc} , $\mathbf{i} = (i_0, i_1, \dots, i_{k_{ldpc}-1})$ onto a codeword of size n_{ldpc} , $\mathbf{c} = (i_0, i_1, \dots, i_{k_{ldpc}-1}, p_0, p_1, \dots, p_{n_{ldpc}-k_{ldpc}-1})$. The transmission of the codeword starts in the given order from i_0 and ends with $p_{n_{ldpc}-k_{ldpc}-1}$.

LDPC code parameters (n_{ldpc}, k_{ldpc}) are given in **Tables 5a** and **5b**.

5.3.2.1 Inner coding for normal FECFRAME

The task of the encoder is to determine $n_{ldpc} - k_{ldpc}$ parity bits $(p_0, p_1, \dots, p_{n_{ldpc}-k_{ldpc}-1})$ for every block of k_{ldpc} information bits, $(i_0, i_1, \dots, i_{k_{ldpc}-1})$. The procedure is as follows:

- Initialise $p_0 = p_1 = p_2 = \dots = p_{n_{ldpc}-k_{ldpc}-1} = 0$
- Accumulate the first information bit, i_0 , at parity bit addresses specified in the first row of **Tables B1** through **B8** in **Annex B**. For example, for rate 2/3 (**Table B3**), (All additions are in GF(2))

$$\begin{array}{ll}
 p_0 = p_0 \oplus i_0 & p_{2767} = p_{2767} \oplus i_0 \\
 p_{10491} = p_{10491} \oplus i_0 & p_{240} = p_{240} \oplus i_0 \\
 p_{16043} = p_{16043} \oplus i_0 & p_{18673} = p_{18673} \oplus i_0 \\
 p_{506} = p_{506} \oplus i_0 & p_{9279} = p_{9279} \oplus i_0 \\
 p_{12826} = p_{12826} \oplus i_0 & p_{10579} = p_{10579} \oplus i_0 \\
 p_{8065} = p_{8065} \oplus i_0 & p_{20928} = p_{20928} \oplus i_0 \\
 p_{8226} = p_{8226} \oplus i_0 &
 \end{array}$$

- For the next 359 information bits, $i_m, m = 1, 2, \dots, 359$ accumulate i_m at parity bit addresses $\{x + m \bmod 360 \times q\} \bmod (n_{ldpc} - k_{ldpc})$ where x denotes the address of the parity bit accumulator corresponding to the first bit i_0 , and q is a code rate dependent constant specified in **Table 7a**. Continuing with the example, $q = 60$ for rate 2/3. So for example for information bit i_1 , the following operations are performed,

$$\begin{array}{ll}
 p_{60} = p_{60} \oplus i_1 & p_{2827} = p_{2827} \oplus i_1 \\
 p_{10551} = p_{10551} \oplus i_1 & p_{300} = p_{300} \oplus i_1 \\
 p_{16103} = p_{16103} \oplus i_1 & p_{18733} = p_{18733} \oplus i_1 \\
 p_{566} = p_{566} \oplus i_1 & p_{9339} = p_{9339} \oplus i_1 \\
 p_{12886} = p_{12886} \oplus i_1 & p_{10639} = p_{10639} \oplus i_1 \\
 p_{8125} = p_{8125} \oplus i_1 & p_{20988} = p_{20988} \oplus i_1 \\
 p_{8286} = p_{8286} \oplus i_1 &
 \end{array}$$

- For the 361st information bit i_{360} , the addresses of the parity bit accumulators are given in the second row of the **Tables B1** through **B8** in **Annex B**. In a similar manner the addresses of the parity bit accumulators for the following 359 information bits $i_m, m = 361, 362, \dots, 719$ are obtained using the formula $\{x + (m \bmod 360) \times q\} \bmod (n_{ldpc} - k_{ldpc})$ where x denotes the address of the parity bit accumulator corresponding to the information bit i_{360} , i.e. the entries in the second row of the **Tables B1** through **B8** in **Annex B**.

- In a similar manner, for every group of 360 new information bits, a new row from **Tables B1 through B8 in Annex B** are used to find the addresses of the parity bit accumulators.

After all of the information bits are exhausted, the final parity bits are obtained as follows,

- Sequentially perform the following operations starting with $i = 1$

$$p_i = p_i \oplus p_{i-1}, \quad i = 1, 2, \dots, n_{ldpc} - k_{ldpc} - 1$$

- Final content of p_i , $i = 0, 1, \dots, n_{ldpc} - k_{ldpc} - 1$ is equal to the parity bit p_i .

Code Rate	q
1/2	90
3/5	72
2/3	60
3/4	45
4/5	36
5/6	30
8/9	20
9/10	18

5.3.2.2 Inner coding for short FECFRAME

k_{ldpc} BCH encoded bits shall be preceded by $k_{ldpcm} - k_{ldpc}$ dummy zeros, where k_{ldpcm} are the input bits of the inner mother code. Resulting k_{ldpcm} bits shall be systematically encoded to generate n_{ldpcm} bits as described in [5.3.2.1: Inner coding for normal FECFRAME], replacing k_{ldpc} with k_{ldpcm} , n_{ldpc} with n_{ldpcm} and the **Tables of Annex B** with the **Tables of Annex C**.

After inner coding, the $k_{ldpcm} - k_{ldpc}$ dummy zeros shall be deleted and the resulting $n_{ldpc} = 16200$ bits shall be transmitted. (Note: $k_{ldpcm} - k_{ldpc} = n_{ldpcm} - n_{ldpc}$).

5.3.3 Bit Interleaver (for 8PSK, 16APSK and 32APSK only)

For 8-PSK, 16-APSK, and 32-APSK modulation formats, the output of the LDPC encoder shall be bit interleaved using a block interleaver. Data is serially written into the interleaver column-wise, and serially read out row-wise (the MSB of BBHEADER is read out first, except 8-PSK rate 3/5 case where LSB of BBHEADER is read out first and MSB is read out last) as shown in **Figure 7**.

The configuration of the block interleaver for each modulation format is specified in **Table 8**.

Modulation	Rows (for $n_{ldpc}=64800$)	Rows (for $n_{ldpc}=16200$)	Columns
8-PSK	21600	5400	3
16-APSK	16200	4050	4
32-APSK	12960	3240	5

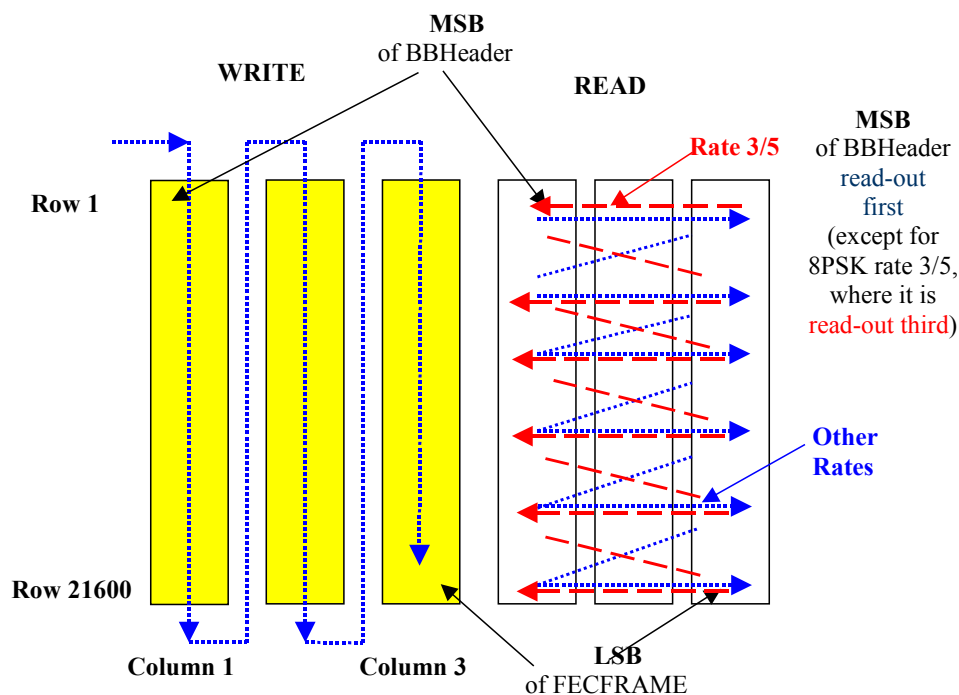


Figure 7: Bit Interleaving scheme for 8PSK and normal FECFRAME length.

5.4 Bit mapping into constellation

Each FECFRAME (which is a sequence of 64800 bits for normal FECFRAME, or 16200 bits for short FECFRAME), shall be serial-to-parallel converted (parallelism level= η_{MOD} : 1 for BPSK, 2 for QPSK, 3 for 8PSK, 4 for 4-12APSK, 5 for 32APSK): in the following **figures 8 to 12**, the MSB of the FECFRAME is mapped into the MSB of the first parallel sequence. Each parallel sequence shall be mapped into constellation, generating a (I,Q) sequence of variable length depending on the selected modulation efficiency η_{MOD} .

The input sequence shall be a FECFRAME, the output sequence shall be a XFECFRAME (complex FECFRAME), composed of $64800/\eta_{MOD}$ (normal XFECFRAME) or $16200/\eta_{MOD}$ (short XFECFRAME) modulation symbols. Each modulation symbol shall be a complex vector in the format (I,Q) (I being the in-phase component and Q the quadrature component) or in the equivalent format $\rho \exp(j\phi)$ (ρ being the modulus of the vector and ϕ being its phase).

5.4.1 Bit mapping into BPSK constellation

Bit mapping into BPSK constellation shall follow **Figure 8**. The normalised average energy per symbol shall be equal to $\rho=1$.

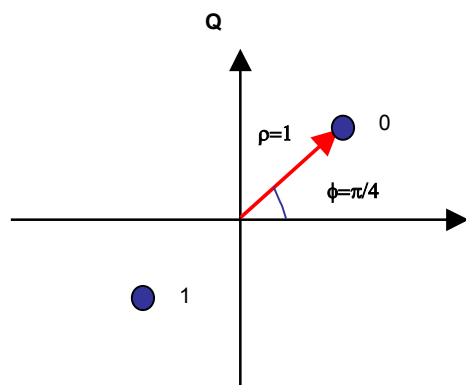


Figure 8: Bit mapping into BPSK constellation

5.4.2 Bit mapping into QPSK constellation

For QPSK, the System shall employ conventional Gray-coded QPSK modulation with absolute mapping (no differential coding). Bit mapping into the QPSK constellation shall follow **Figure 9**. The normalised average energy per symbol shall be equal to $\rho=1$.

Two FECFRAME bits are mapped to a QPSK symbol i.e. bits $2i$ and $2i+1$ determines the i^{th} QPSK symbol, where $i=0,1,2,\dots,(N/2)-1$ and N is the coded LDPC block size.

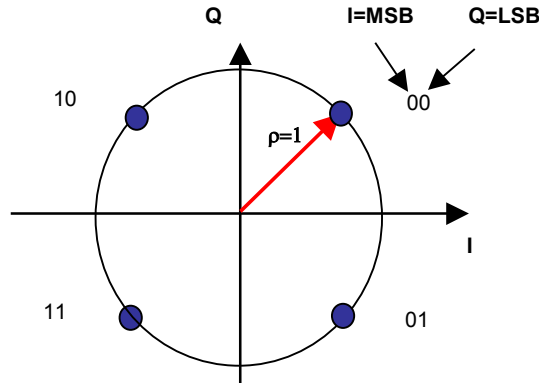


Figure 9: Bit mapping into QPSK constellation

5.4.3 Bit mapping into 8PSK constellation

For 8PSK, the System shall employ conventional Gray-coded 8PSK modulation with absolute mapping (no differential coding). Bit mapping into the 8PSK constellation shall follow **Figure 9**. The normalised average energy per symbol shall be equal to $\rho=1$.

For all the rates excluding 3/5, bits $3i$, $3i+1$, $3i+2$ of the interleaver output determine the i^{th} 8-PSK symbol where $i=0,1,2,\dots,(N/3)-1$ and N is the coded LDPC block size. For rate 3/5 bits $3i+2$, $3i+1$, $3i$ of the interleaver output determine the i^{th} 8-PSK symbol where $i=0,1,2,\dots,(N/3)-1$ and N is the coded LDPC block size.

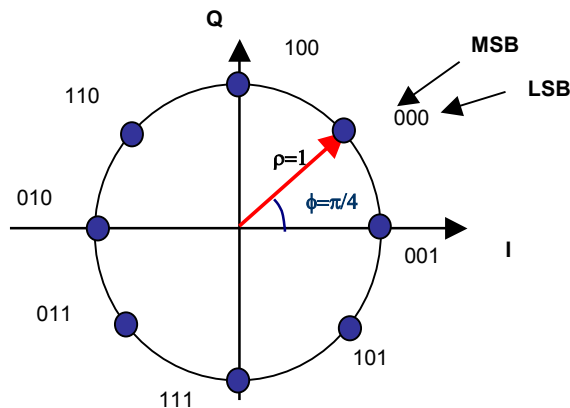


Figure 10: Bit mapping into 8PSK constellation

5.4.4 Bit mapping into 16APSK constellation

The 16APSK modulation constellation (**Figure 11**) shall be composed of two concentric rings of uniformly spaced 4 and 12 PSK points, respectively in the inner ring of radius R_1 and outer ring of radius R_2 .

The ratio of the outer circle radius to the inner circle radius ($\gamma = R_2/R_1$) shall comply with **Table 9** [see **Annex H**, Bibliography 1].

If $4[R_1]^2 + 12[R_2]^2 = 16$ the average signal energy becomes 1.

Bits $4i, 4i+1, 4i+2$ and $4i+3$ of the interleaver output determine the i^{th} 16APSK symbol, where $i=0,1,2,\dots,(N/4)-1$ and N is the coded LDPC block size.

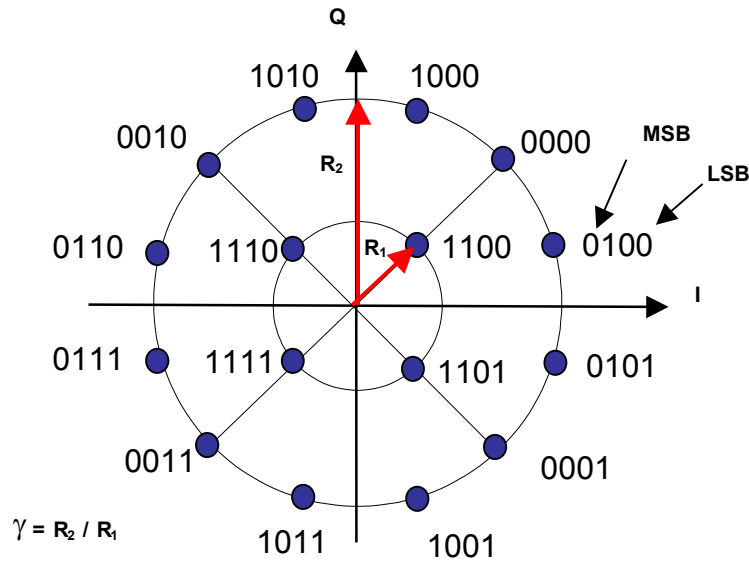


Figure 11: 16APSK signal constellation

Table 9: optimum constellation radius ratio γ (linear channel) for 16APSK		
Code rate	Modulation spectral efficiency	γ
2/3	2.66	3.15
3/4	2.99	2.85
5/6	3.32	2.70
8/9	3.55	2.60
9/10	3.59	2.57

5.4.5 Bit mapping into 32APSK

The 32APSK modulation constellation (see **Fig. 12**) shall be composed of three concentric rings of uniformly spaced 4, 12 and 16 PSK points, respectively in the inner ring of radius R_1 , the intermediate ring of radius R_2 and the outer ring or radius R_3 . **Table 10** defines the values of $\gamma_1 = R_2/R_1$ and $\gamma_2 = R_3/R_1$.

If $4[R_1]^2 + 12[R_2]^2 + 16[R_3]^2 = 32$ the average signal energy becomes equal to 1.

Bits $5i, 5i+1, 5i+2, 5i+3$ and $5i+4$ of the interleaver output determine the i^{th} 32APSK symbol, where $i=0, 1, 2, \dots, (N/5)-1$.

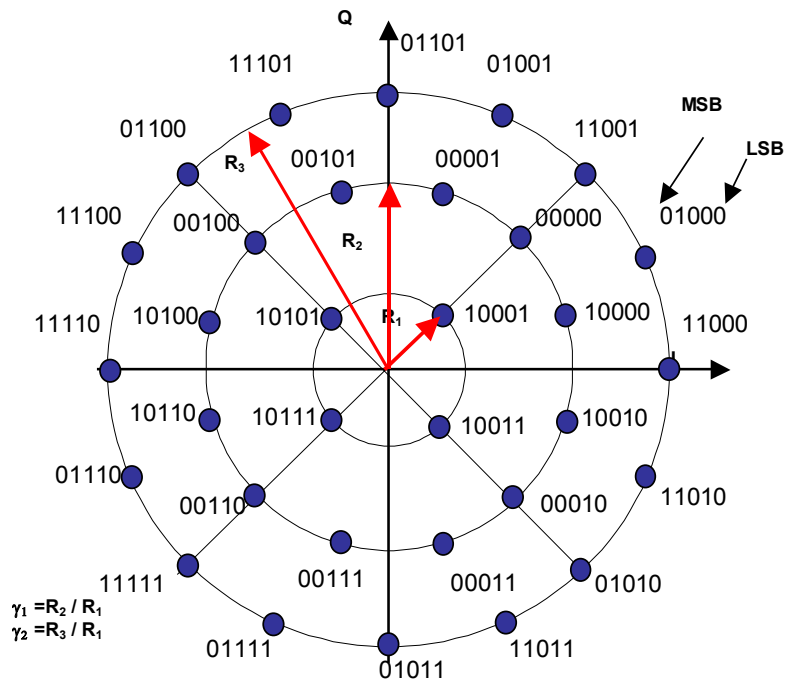


Figure 12: 32APSK signal constellation

Code rate	Modulation spectral efficiency	γ_1	γ_2
3/4	3.74	2.84	5.27
4/5	3.99	2.72	4.87
5/6	4.165	2.64	4.64
8/9	4.424	2.54	4.33

5.5. Physical Layer (PL) Framing

The PLFraming sub-system shall generate a physical layer frame (named PLFRAME) by performing the following processes (see **Figures 1** and **13**):

- Dummy PLFRAME generation when no XFECFRAME is ready to be processed and transmitted.
- XFECFRAME slicing into an integer number S of constant length SLOTS (length: M=90 symbols each); S shall be according to **Table 11**,
- PLHEADER generation and insertion before the XFECFRAME for receiver configuration. PLHEADER shall occupy exactly one SLOT (length: M=90 Symbols)
- Pilot Block insertion (for modes requiring pilots) every 16 SLOTS, to help receiver synchronisation. The Pilot Block shall be composed of P=36 pilot symbols.
- randomisation of the (I,Q) modulated symbols by means of a physical layer scrambler

The input stream of the sub-system shall be a XFECFRAME and the output a scrambled PLFRAME

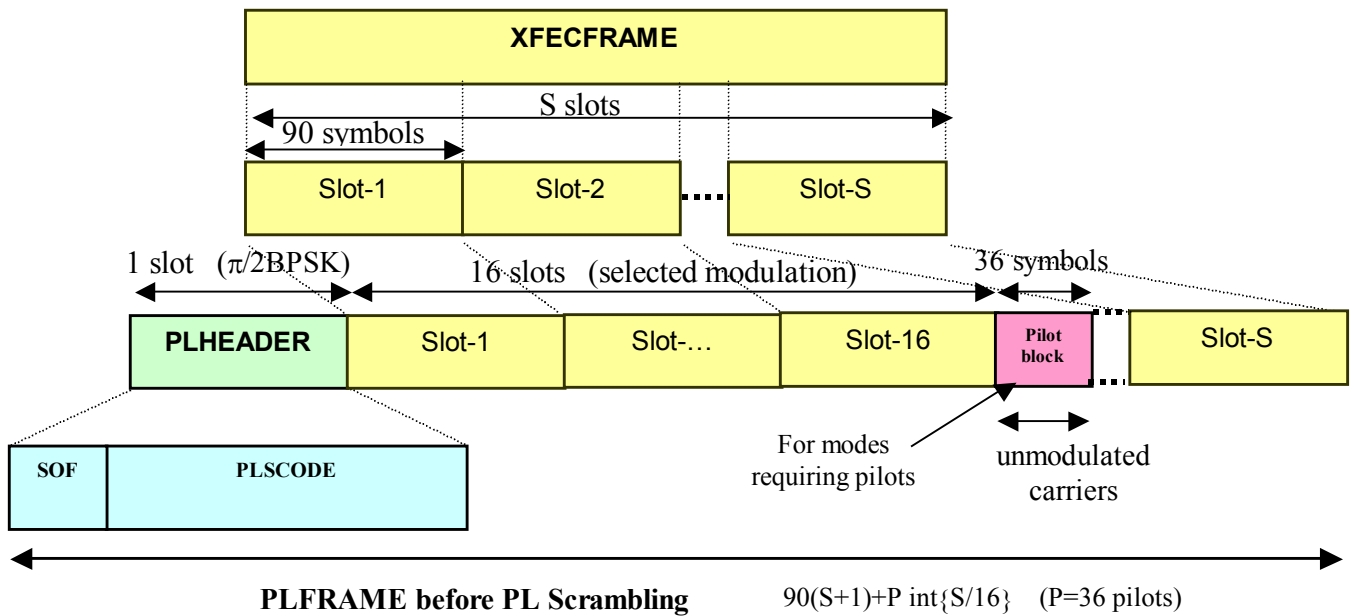


Figure 13. Format of a “Physical Layer Frame” PLFRAME

Table 11: S= number of SLOTS (M=90 symbols) per XFECFRAME

η_{MOD} (bit/s/Hz)	$n_{\text{ldpc}}=64800$ (normal frame)		$n_{\text{ldpc}}=16200$ (short frame)	
	S	η % no-pilot	S	η % no-pilot
1	720	99.86	180	99.45
2	360	99.72	90	98.90
3	240	99.58	60	98.36
4	180	99.45	45	97.83
5	144	99.31	36	97.30

The PLFRAMING efficiency is : $\eta = 90S / [90(S+1) + P \text{int}\{(S-1)/16\}]$ where P=36 and $\text{int}\{.\}$ is the integer function

5.5.1 Dummy PLFRAME insertion

A Dummy PLFRAME shall be composed of a PLHEADER (see section 5.5.2) and of 36 SLOTS of un-modulated carriers (I=+1, Q=0).

5.5.2 PL Signalling

The PLHEADER is intended for receiver synchronisation and physical layer signalling (note: after decoding the PLHEADER, the receiver knows the PLFRAME duration and structure, the modulation and coding scheme of the XFECFRAME, the presence or absence of pilot symbols). The PLHEADER (one SLOT of 90 symbols) shall be composed of the following fields:

- **SO**F (26 symbols), identifying the Start of Frame
- **PL**S code (64 symbol): PLS (Physical Layer Signalling) code shall be a non-systematic binary code of length 64 and dimension 7 with minimum distance $d_{\text{min}}=32$. It is equivalent to the first order Reed-Muller under permutation. It transmits 7 bits for physical layer signalling purpose. These 7 bits consists of two fields: MODCOD and TYPE defined as follows:
 - **MOD**CO**D** (5 symbols), identifying the XFECFRAME modulation and FEC rate
 - **TY**PE (2 symbols), identifying the FECFRAME length (64800 or 16200 bits) and the presence/absence of pilots

The PLHEADER, represented by the binary sequence $(y_1, y_2, \dots, y_{90})$ shall be modulated into 90 $\pi/2$ BPSK symbols according to the rule:

$$I_{2i-1} = Q_{2i-1} = (1/\sqrt{2})(1-2y_{2i-1}), \quad I_{2i} = -Q_{2i} = -(1/\sqrt{2})(1-2y_{2i}) \quad \text{for } i=1,2,\dots,90$$

5.5.2.1 SOF field

SOF shall correspond to the sequence 18D2E82_{Hex} (01-1000-....-0010 in binary notation, the left-side bit being the MSB of the PLHEADER).

5.5.2.2 MODCOD field

MODCOD shall correspond to 5 bits, identifying code rates in the set $\eta_C = [1/2, 3/5, 2/3, 3/4, 4/5, 5/6, 8/9, 9/10]$ and modulations in the set of spectrum efficiencies $\eta_{MOD} = [1, 2, 3, 4, 5]$ according to **Table 12**.

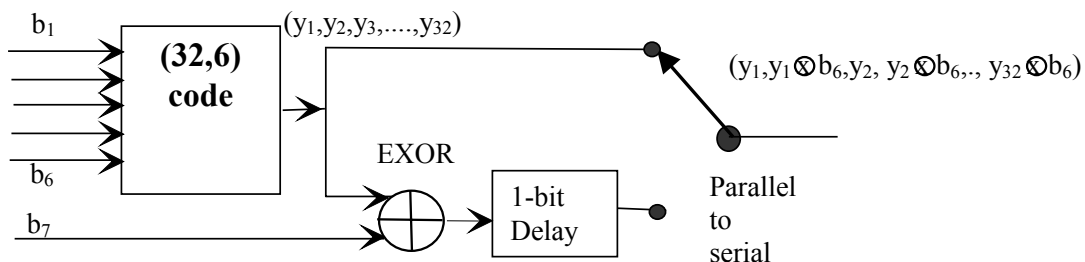
Mode	MOD COD	Mode	MOD COD	Mode	MOD COD	Mode	MOD COD
BPSK 1/2	1 _D	QPSK 5/6	9 _D	8PSK 9/10	17 _D	32APSK 4/5	25 _D
BPSK 3/5	2 _D	QPSK 8/9	10 _D	16APSK 2/3	18 _D	32APSK 5/6	26 _D
BPSK 2/3	3 _D	QPSK 9/10	11 _D	16APSK 3/4	19 _D	32APSK 8/9	27 _D
QPSK 1/2	4 _D	8PSK 3/5	12 _D	16APSK 4/5	20 _D	reserved	28 _D
QPSK 3/5	5 _D	8PSK 2/3	13 _D	16APSK 5/6	21 _D	reserved	29 _D
QPSK 2/3	6 _D	8PSK 3/4	14 _D	16APSK 8/9	22 _D	reserved	30 _D
QPSK 3/4	7 _D	8PSK 5/6	15 _D	16APSK 9/10	23 _D	reserved	31 _D
QPSK 4/5	8 _D	8PSK 8/9	16 _D	32APSK 3/4	24 _D	DUMMY PLFRAME	0 _D

5.5.2.3 TYPE field

The MSB of the TYPE field shall identify 2 FECFRAME sizes (0=normal: 64800 bits; 1=short: 16200 bits). The LSB of the TYPE field shall identify the pilot configurations (see **Section 5.5.3**) (0=no pilots, 1=pilots).

5.5.2.4 PLS code

The MODCODE field is bi-orthogonally coded with a (64, 7) code. Such code is constructed starting from a bi-orthogonal (32,6) code according to the construction in the next figure.



The particular construction guarantees that each odd bit in the (64,7) code is either always equal to the previous one or is always the opposite. Which of the two hypotheses is true depends on the bit b_7 . This fact can be exploited in case differentially coherent detection is adopted in the receiver.

The MODCOD and the MSB of the TYPE field shall be encoded by a linear block code of length 32 with the following generator matrix:

$$G = \begin{bmatrix} 01010101010101010101010101010101 \\ 00110011001100110011001100110011 \\ 00001111000011110000111100001111 \\ 00000000111111110000000011111111 \\ 00000000000000001111111111111111 \\ 11111111111111111111111111111111 \end{bmatrix}$$

The most significant bit of the MODCOD is multiplied with the first row of the matrix, the following bit with the second row and so on. The 32 coded bits is denoted as $(y_1 y_2 \dots y_{32})$. When the least significant bit of the TYPE field is 0, the final PLS code will generate $(y_1 y_1 y_2 y_2 \dots y_{32} y_{32})$ as the output, i.e., each symbol shall be repeated. When the least significant bit of the TYPE field is 1, the final PLS code will generate $(y_1 \bar{y}_1 y_2 \bar{y}_2 \dots y_{32} \bar{y}_{32})$ as output, i.e., the repeated symbol is further binary complemented. The 64 bits output of the PLS code is further scrambled by the binary sequence:

0111000110011101100000111100100101010011010000100010110111111010.

5.5.3 Pilots insertion

Two PLFRAME configurations shall be possible:

- Without pilots

With pilots.

In this latter case a PILOT BLOCK shall be composed of $P=36$ pilot symbols. Each pilot shall be an un-modulated symbol, identified by $I=(1/\sqrt{2})$, $Q=(1/\sqrt{2})$. The first PILOT BLOCK shall be inserted 16 SLOTS after the PLHEADER, the second after 32 SLOTS and so on, as represented in Figure 13. If the PILOT BLOCK position coincides with the beginning of the next SOF, then the PILOT BLOCK is not transmitted.

5.5.4 Physical layer scrambling

Prior to modulation, each PLFRAME, excluding the PLHEADER, shall be randomised for energy dispersal by multiplying the $(I+jQ)$ samples by a complex randomisation sequence (C_I+jC_Q) :

$$I_{\text{SCRAMBLED}} = [I C_I - Q C_Q]; \quad Q_{\text{SCRAMBLED}} = (I C_Q + Q C_P)$$

(Notes: The randomization sequence rate corresponds to the I-Q PLFRAME symbol rate, thus it has no impact on the occupied signal bandwidth. The randomization sequence has a period greater than the maximum required duration of about 70000 symbols. BPSK constellations are transformed into a complex QPSK constellation, thus reducing the signal envelope fluctuations).

The randomization sequence shall be reinitialized at the end of each PLHEADER (see **Figure 14**). The PLFRAME duration depends on the modulation selected, thus the randomization sequence length shall be truncated to the current PLFRAME length.

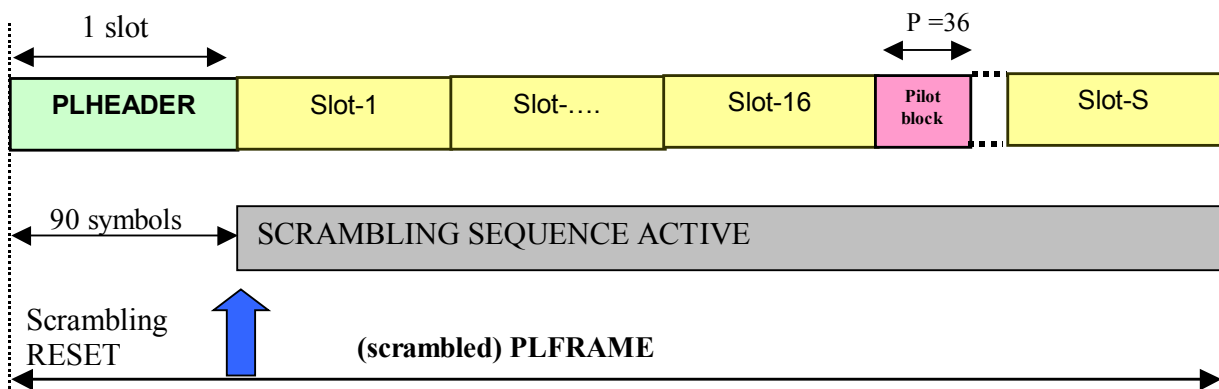


Figure 14: PL SCRAMBLING

The scrambling code sequences shall be constructed by combining two real m-sequences (generated by means of two generator polynomials of degree 18) into a complex sequence. The resulting sequences thus constitute segments of a set of Gold sequences.

Let x and y be the two sequences respectively. The x sequence is constructed using the primitive (over GF(2)) polynomial $1+X^7+X^{18}$. The y sequence is constructed using the polynomial $1+X^5+X^7+X^{10}+X^{18}$.

The sequence depending on the chosen scrambling code number n is denoted z_n , in the sequel.

Furthermore, let $x(i)$, $y(i)$ and $z_n(i)$ denote the i^{th} symbol of the sequence x , y , and z_n , respectively.

The m -sequences x and y are constructed as:

Initial conditions:

$$\begin{aligned} x \text{ is constructed with } x(0)=1, x(1)=x(2)=\dots=x(16)=x(17)=0. \\ y(0)=y(1)=\dots=y(16)=y(17)=1. \end{aligned}$$

Recursive definition of subsequent symbols:

$$\begin{aligned} x(i+18) &= x(i+7) + x(i) \text{ modulo } 2, i=0, \dots, 2^{18}-20. \\ y(i+18) &= y(i+10)+y(i+7)+y(i+5)+y(i) \text{ modulo } 2, i=0, \dots, 2^{18}-20. \end{aligned}$$

The n^{th} Gold code sequence z_n , $n=0,1,2, \dots, 2^{18}-2$, is then defined as:

$$z_n(i) = [x((i+n) \text{ modulo } (2^{18} - 1)) + y(i)] \text{ modulo } 2, i=0, \dots, 2^{18}-2.$$

These binary sequences are converted to integer valued sequences Z_n (Z_n assuming values 0,1,2,3) by the following transformation:

$$R_n(i) = 2 z_n((i+131072) \text{ modulo } (2^{18}-1)) + z_n(i) \quad i=0,1, \dots, 66419.$$

Finally, the n^{th} complex scrambling code sequence $C_I(i)+jC_Q(i)$ is defined as:

$$C_I(i)+jC_Q(i) = \exp(j R_n(i) \pi/2)$$

R_n	$\exp(j R_n \pi/2)$	$I_{\text{scrambled}}$	$Q_{\text{scrambled}}$
0	1	I	Q
1	j	-Q	I
2	-1	-I	-Q
3	-j	Q	-I

Figure 15 gives a possible block diagram for PL scrambling sequences generation for $n=0$.

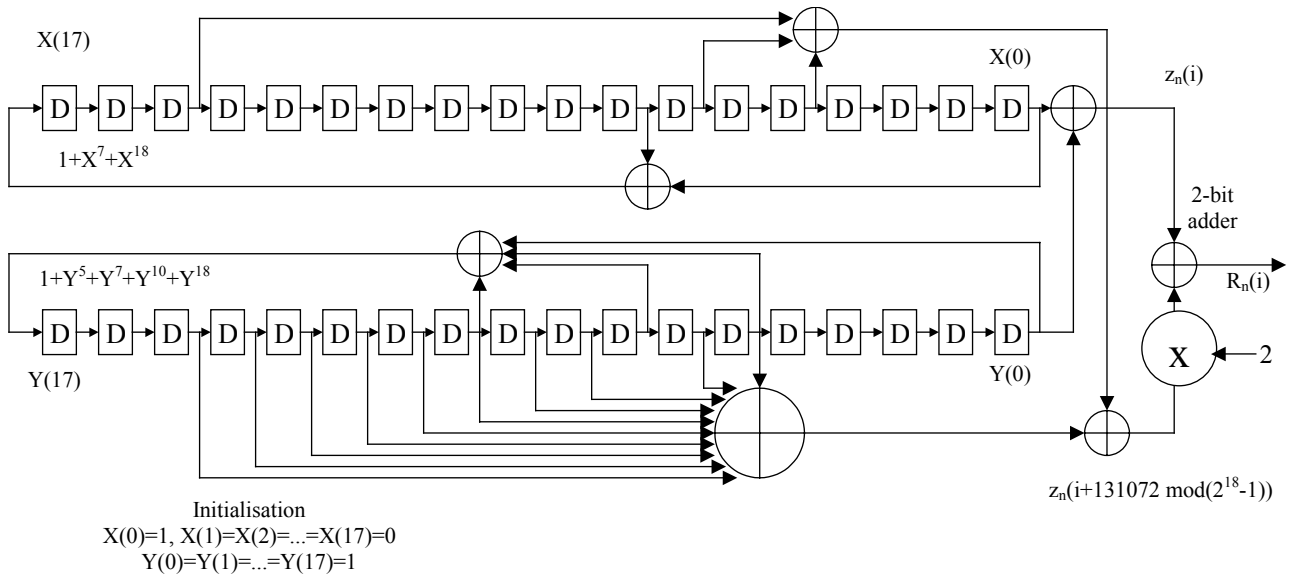


Figure 15: Configuration of PL scrambling code generator for $n=0$

In case of broadcasting services, $n=0$ shall be used as default sequence, to avoid manual receiver setting or synchronisation delays.

(Note: n , assuming values in the range 0, 262141, indicates the spreading sequence number. The use of different PL Scrambling sequences allows a reduction of interference correlation between different services. For the same purpose, it is possible to reuse a shifted version of the same sequence in different satellite beams. Furthermore n could be unequivocally associated to each satellite operator or satellite or transponder, thus permitting identification of an interfering signal via the PL Scrambling “signature” detection.)

5.6 Baseband shaping and quadrature modulation

After randomisation, the signals shall be square root raised cosine filtered. The roll-off factor shall be $\alpha= 0.35, 0.25$ and 0.20 , depending on the service requirements.

The baseband square root raised cosine filter shall have a theoretical function defined by the following expression:

$$H(f) = 1 \quad \text{for } |f| < f_N(1 - \alpha)$$

$$H(f) = \left\{ \frac{1}{2} + \frac{1}{2} \sin \frac{\pi}{2f_N} \left[\frac{f_N - |f|}{\alpha} \right] \right\}^{1/2} \quad \text{for } f_N(1 - \alpha) \leq |f| \leq f_N(1 + \alpha)$$

$$H(f) = 0 \quad \text{for } |f| > f_N(1 + \alpha),$$

where

$$f_N = \frac{1}{2T_s} = \frac{R_s}{2}$$

is the Nyquist frequency and α is the roll-off factor.

A template for the signal spectrum at the modulator output is given in **Annex A**.

6 Error performance

Table 13 summarizes performance requirements at QEF over AWGN (E_s = average energy per transmitted symbol). Ideal E_s/No (dB) is the figure achieved by computer simulation, perfect carrier and synchronisation recovery, no phase noise, AWGN channel. For short FECFRAMEs an additional degradation of 0.2-0.3 dB has to be taken into account.

For calculating link budgets, specific satellite channel impairments should be taken into account. **Table G1** in **Annex G** gives examples of possible degradation introduced by the satellite channel model described in **Section G7**.

PER is the ratio between the useful transport stream packets (188 bytes) correctly received and affected by errors, after forward error correction.

Table 13 E_s/No performance at Quasi Error Free PER= 10^{-7} (AWGN channel)		
Mode	Spectral efficiency	Ideal E_s/No (dB) for FECFRAME length=64800
BPSK 1/2	0,495114	-2.00
BPSK 3/5	0,594976	-0.77
BPSK 2/3	0,662042	0.10
QPSK 1/2	0,988857	1.00
QPSK 3/5	1,188303	2.23
QPSK 2/3	1,322251	3.10
QPSK 3/4	1,487472	4.03
QPSK 4/5	1,587195	4.68
QPSK 5/6	1,654662	5.18
QPSK 8/9	1,766451	6.20
QPSK 9/10	1,788612	6.42
8PSK 3/5	1,779989	5.50
8PSK 2/3	1,980633	6.62
8PSK 3/4	2,228122	7.91
8PSK 5/6	2,478560	9.35
8PSK 8/9	2,646012	10.69
8PSK 9/10	2,679207	10.98
16APSK 2/3	2,637197	8.97
16APSK 3/4	2,966726	10.21
16APSK 4/5	3,165621	11.03
16APSK 5/6	3,300181	11.61
16APSK 8/9	3,523142	12.89
16APSK 9/10	3,567341	10.13
32APSK 3/4	3,703293	12.73
32APSK 4/5	3,951568	13.64
32APSK 5/6	4,119537	14.28
32APSK 8/9	4,397854	15.69

Spectral efficiencies (per unit symbol rate) are computed for normal FECFRAME length and no pilots. Examples of possible use of the System are given in **Annex G**.

Annex A (normative): Signal spectrum at the modulator output

For roll-off factor $\alpha=0$, the signal spectrum at the modulator output shall be in accordance with EN 300 421.

As an option, the signal spectrum can correspond to a narrower roll-off factor $\alpha=0,25$ or 0.20 .

Figure A.1 gives a template for the signal spectrum at the modulator output for a roll-off factor $\alpha=0,35$ and 0.25 .

Figure A.1 also represents a possible mask for a hardware implementation of the Nyquist modulator filter. The points A to S shown on **Figures A.1** and **A.2** are defined in **Table A.1** for roll-off factors $\alpha=0,35$ and $\alpha=0,25$. The mask for the filter frequency response is based on the assumption of ideal Dirac delta input signals, spaced by the symbol period $T_s = 1/R_s = 1/2f_N$, while in the case of rectangular input signals a suitable $x/\sin x$ correction shall be applied on the filter response.

Figure A.2 gives a mask for the group delay for the hardware implementation of the Nyquist modulator filter.

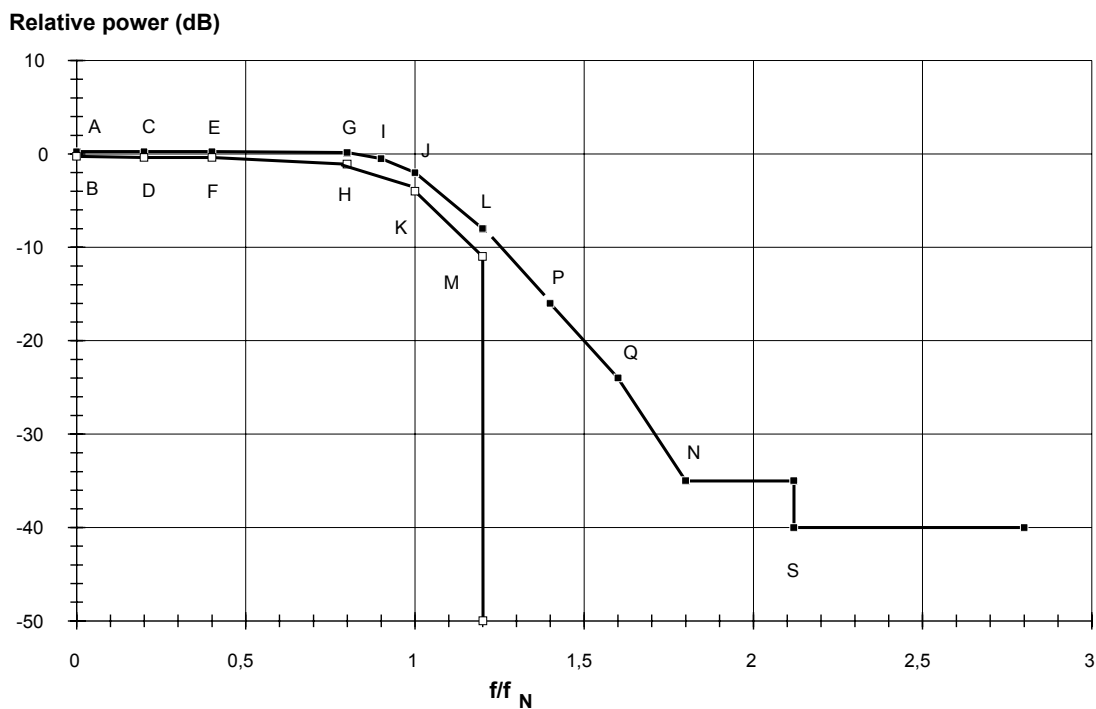


Figure A.1: Template for the signal spectrum mask at the modulator output represented in the baseband frequency domain (roll-off factor $\alpha=0,35$ and 0.25). The frequency axis is calibrated for $\alpha=0,35$.

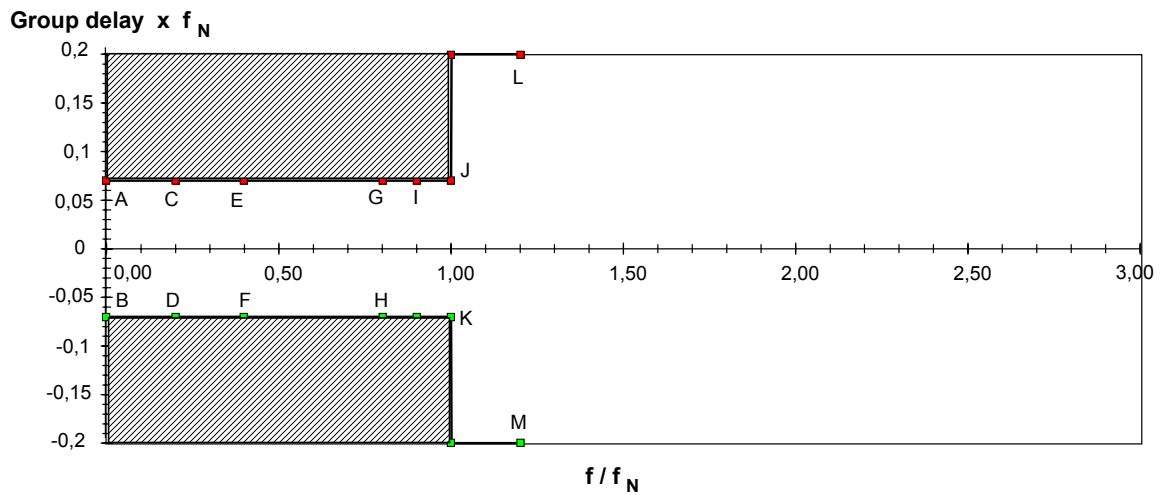


Figure A.2: Template of the modulator filter group delay (roll-off factors $\alpha=0,35$ and $\alpha=0,25$)

Table A.1: Definition of points given in **Figures A.1** and **A.2**

Point	Frequency for $\alpha=0,35$	Frequency for $\alpha=0,25$	Relative power (dB)	Group delay
A	$0,0 f_N$	$0,0 f_N$	+0,25	$+0,07 / f_N$
B	$0,0 f_N$	$0,0 f_N$	-0,25	$-0,07 / f_N$
C	$0,2 f_N$	$0,2 f_N$	+0,25	$+0,07 / f_N$
D	$0,2 f_N$	$0,2 f_N$	-0,40	$-0,07 / f_N$
E	$0,4 f_N$	$0,4 f_N$	+0,25	$+0,07 / f_N$
F	$0,4 f_N$	$0,4 f_N$	-0,40	$-0,07 / f_N$
G	$0,8 f_N$	$0,86 f_N$	+0,15	$+0,07 / f_N$
H	$0,8 f_N$	$0,86 f_N$	-1,10	$-0,07 / f_N$
I	$0,9 f_N$	$0,93 f_N$	-0,50	$+0,07 / f_N$
J	$1,0 f_N$	$1,0 f_N$	-2,00	$+0,07 / f_N$
K	$1,0 f_N$	$1,0 f_N$	-4,00	$-0,07 / f_N$
L	$1,2 f_N$	$1,13 f_N$	-8,00	-
M	$1,2 f_N$	$1,13 f_N$	-11,00	-
N	$1,8 f_N$	$1,60 f_N$	-35,00	-
P	$1,4 f_N$	$1,30 f_N$	-16,00	-
Q	$1,6 f_N$	$1,45 f_N$	-24,00	-
S	$2,12 f_N$	$1,83 f_N$	-40,00	-

Annex B (normative): Addresses of parity bit accumulators for $n_{ldpc}=64800$

Table B1 (Rate 1/2)

54 9318 14392 27561 26909 10219 2534 8597	89 26403 25168 19038 18384 8882 12719 7093	34 12280 26611
55 7263 4635 2530 28130 3033 23830 3651	0 14567 24965	35 6526 26122
56 24731 23583 26036 17299 5750 792 9169	1 3908 100	36 26165 11241
57 5811 26154 18653 11551 15447 13685 16264	2 10279 240	37 7666 26962
58 12610 11347 28768 2792 3174 29371 12997	3 24102 764	38 16290 8480
59 16789 16018 21449 6165 21202 15850 3186	4 12383 4173	39 11774 10120
60 31016 21449 17618 6213 12166 8334 18212	5 13861 15918	40 30051 30426
61 22836 14213 11327 5896 718 11727 9308	6 21327 1046	41 1335 15424
62 2091 24941 29966 23634 9013 15587 5444	7 5288 14579	42 6865 17742
63 22207 3983 16904 28534 21415 27524 25912	8 28158 8069	43 31779 12489
64 25687 4501 22193 14665 14798 16158 5491	9 16583 11098	44 32120 21001
65 4520 17094 23397 4264 22370 16941 21526	10 16681 28363	45 14508 6996
66 10490 6182 32370 9597 30841 25954 2762	11 13980 24725	46 979 25024
67 22120 22865 29870 15147 13668 14955 19235	12 32169 17989	47 4554 21896
68 6689 18408 18346 9918 25746 5443 20645	13 10907 2767	48 7989 21777
69 29982 12529 13858 4746 30370 10023 24828	14 21557 3818	49 4972 20661
70 1262 28032 29888 13063 24033 21951 7863	15 26676 12422	50 6612 2730
71 6594 29642 31451 14831 9509 9335 31552	16 7676 8754	51 12742 4418
72 1358 6454 16633 20354 24598 624 5265	17 14905 20232	52 29194 595
73 19529 295 18011 3080 13364 8032 15323	18 15719 24646	53 19267 20113
74 11981 1510 7960 21462 9129 11370 25741	19 31942 8589	
75 9276 29656 4543 30699 20646 21921 28050	20 19978 27197	
76 15975 25634 5520 31119 13715 21949 19605	21 27060 15071	
77 18688 4608 31755 30165 13103 10706 29224	22 6071 26649	
78 21514 23117 12245 26035 31656 25631 30699	23 10393 11176	
79 9674 24966 31285 29908 17042 24588 31857	24 9597 13370	
80 21856 27777 29919 27000 14897 11409 7122	25 7081 17677	
81 29773 23310 263 4877 28622 20545 22092	26 1433 19513	
82 15605 5651 21864 3967 14419 22757 15896	27 26925 9014	
83 30145 1759 10139 29223 26086 10556 5098	28 19202 8900	
84 18815 16575 2936 24457 26738 6030 505	29 18152 30647	
85 30326 22298 27562 20131 26390 6247 24791	30 20803 1737	
86 928 29246 21246 12400 15311 32309 18608	31 11804 25221	
87 20314 6025 26689 16302 2296 3244 19613	32 31683 17783	
88 6237 11943 22851 15642 23857 15112 20947	33 29694 9345	

Example interpretation of the Table

$$p_{54} = p_{54} \oplus i_0 \quad p_{9318} = p_{9318} \oplus i_0 \quad p_{14392} = p_{14392} \oplus i_0 \quad p_{27561} = p_{27561} \oplus i_0 \quad p_{26909} = p_{26909} \oplus i_0$$

$$p_{10219} = p_{10219} \oplus i_0 \quad p_{2534} = p_{2534} \oplus i_0 \quad p_{8597} = p_{8597} \oplus i_0$$

$$p_{144} = p_{144} \oplus i_1 \quad p_{9408} = p_{9408} \oplus i_1 \quad p_{14482} = p_{14482} \oplus i_1 \quad p_{27651} = p_{27651} \oplus i_1 \quad p_{26999} = p_{26999} \oplus i_1$$

$$p_{10309} = p_{10309} \oplus i_1 \quad p_{2624} = p_{2624} \oplus i_1 \quad p_{8687} = p_{8687} \oplus i_1$$

$$\begin{matrix} \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \end{matrix}$$

$$p_{32364} = p_{32364} \oplus i_{359} \quad p_{9228} = p_{9228} \oplus i_{359} \quad p_{14302} = p_{14302} \oplus i_{359} \quad p_{27471} = p_{27471} \oplus i_{359} \quad p_{26819} = p_{26819} \oplus i_{359}$$

$$p_{10129} = p_{10129} \oplus i_{359} \quad p_{2444} = p_{2444} \oplus i_{359} \quad p_{8507} = p_{8507} \oplus i_{359}$$

$$p_{55} = p_{55} \oplus i_{360} \quad p_{7263} = p_{7263} \oplus i_{360} \quad p_{4635} = p_{4635} \oplus i_{360} \quad p_{2530} = p_{2530} \oplus i_{360} \quad p_{28130} = p_{28130} \oplus i_{360}$$

$$p_{3033} = p_{3033} \oplus i_{360} \quad p_{23830} = p_{23830} \oplus i_{360} \quad p_{3651} = p_{3651} \oplus i_{360}$$

$$\begin{matrix} \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \end{matrix}$$

Table B2. (rate 3/5)

22422 10282 11626 19997 11161 2922 3122 99 5625 17064 8270 179	17 22782 5828	70 3564 2925
25087 16218 17015 828 20041 25656 4186 11629 22599 17305 22515 6463	18 19775 4247	71 3434 7769
11049 22853 25706 14388 5500 19245 8732 2177 13555 11346 17265 3069	19 1660 19413	
16581 22225 12563 19717 23577 11555 25496 6853 25403 5218 15925 21766	20 4403 3649	
16529 14487 7643 10715 17442 11119 5679 14155 24213 21000 1116 15620	21 13371 25851	
5340 8636 16693 1434 5635 6516 9482 20189 1066 15013 25361 14243	22 22770 21784	
18506 22236 20912 8952 5421 15691 6126 21595 500 6904 13059 6802	23 10757 14131	
8433 4694 5524 14216 3685 19721 25420 9937 23813 9047 25651 16826	24 16071 21617	
21500 24814 6344 17382 7064 13929 4004 16552 12818 8720 5286 2206	25 6393 3725	
22517 2429 19065 2921 21611 1873 7507 5661 23006 23128 20543 19777	26 597 19968	
1770 4636 20900 14931 9247 12340 11008 12966 4471 2731 16445 791	27 5743 8084	
6635 14556 18865 22421 22124 12697 9803 25485 7744 18254 11313 9004	28 6770 9548	
19982 23963 18912 7206 12500 4382 20067 6177 21007 1195 23547 24837	29 4285 17542	
756 11158 14646 20534 3647 17728 11676 11843 12937 4402 8261 22944	30 13568 22599	
9306 24009 10012 11081 3746 24325 8060 19826 842 8836 2898 5019	31 1786 4617	
7575 7455 25244 4736 14400 22981 5543 8006 24203 13053 1120 5128	32 23238 11648	
3482 9270 13059 15825 7453 23747 3656 24585 16542 17507 22462 14670	33 19627 2030	
15627 15290 4198 22748 5842 13395 23918 16985 14929 3726 25350 24157	34 13601 13458	
24896 16365 16423 13461 16615 8107 24741 3604 25904 8716 9604 20365	35 13740 17328	
3729 17245 18448 9862 20831 25326 20517 24618 13282 5099 14183 8804	36 25012 13944	
16455 17646 15376 18194 25528 1777 6066 21855 14372 12517 4488 17490	37 22513 6687	
1400 8135 23375 20879 8476 4084 12936 25536 22309 16582 6402 24360	38 4934 12587	
25119 23586 128 4761 10443 22536 8607 9752 25446 15053 1856 4040	39 21197 5133	
377 21160 13474 5451 17170 5938 10256 11972 24210 17833 22047 16108	40 22705 6938	
13075 9648 24546 13150 23867 7309 19798 2988 16858 4825 23950 15125	41 7534 24633	
20526 3553 11525 23366 2452 17626 19265 20172 18060 24593 13255 1552	42 24400 12797	
18839 21132 20119 15214 14705 7096 10174 5663 18651 19700 12524 14033	43 21911 25712	
4127 2971 17499 16287 22368 21463 7943 18880 5567 8047 23363 6797	44 12039 1140	
10651 24471 14325 4081 7258 4949 7044 1078 797 22910 20474 4318	45 24306 1021	
21374 13231 22985 5056 3821 23718 14178 9978 19030 23594 8895 25358	46 14012 20747	
6199 22056 7749 13310 3999 23697 16445 22636 5225 22437 24153 9442	47 11265 15219	
7978 12177 2893 20778 3175 8645 11863 24623 10311 25767 17057 3691	48 4670 15531	
20473 11294 9914 22815 2574 8439 3699 5431 24840 21908 16088 18244	49 9417 14359	
8208 5755 19059 8541 24924 6454 11234 10492 16406 10831 11436 9649	50 2415 6504	
16264 11275 24953 2347 12667 19190 7257 7174 24819 2938 2522 11749	51 24964 24690	
3627 5969 13862 1538 23176 6353 2855 17720 2472 7428 573 15036	52 14443 8816	
0 18539 18661	53 6926 1291	
1 10502 3002	54 6209 20806	
2 9368 10761	55 13915 4079	
3 12299 7828	56 24410 13196	
4 15048 13362	57 13505 6117	
5 18444 24640	58 9869 8220	
6 20775 19175	59 1570 6044	
7 18970 10971	60 25780 17387	
8 5329 19982	61 20671 24913	
9 11296 18655	62 24558 20591	
10 15046 20659	63 12402 3702	
11 7300 22140	64 8314 1357	
12 22029 14477	65 20071 14616	
13 11129 742	66 17014 3688	
14 13254 13813	67 19837 946	
15 19234 13273	68 15195 12136	
16 6079 21122	69 7758 22808	

Table B3 (Rate 2/3)

0 10491 16043 506 12826 8065 8226 2767 240 18673 9279 10579 20928	51 18892 4356	43 18429 8472
1 17819 8313 6433 6224 5120 5824 12812 17187 9940 13447 13825 18483	52 7894 3898	44 12093 20753
2 17957 6024 8681 18628 12794 5915 14576 10970 12064 20437 4455 7151	53 5963 4360	45 16345 12748
3 19777 6183 9972 14536 8182 17749 11341 5556 4379 17434 15477 18532	54 7346 11726	46 16023 11095
4 4651 19689 1608 659 16707 14335 6143 3058 14618 17894 20684 5306	55 5182 5609	47 5048 17595
5 9778 2552 12096 12369 15198 16890 4851 3109 1700 18725 1997 15882	56 2412 17295	48 18995 4817
6 486 6111 13743 11537 5591 7433 15227 14145 1483 3887 17431 12430	57 9845 20494	49 16483 3536
7 20647 14311 11734 4180 8110 5525 12141 15761 18661 18441 10569 8192	58 6687 1864	50 1439 16148
8 3791 14759 15264 19918 10132 9062 10010 12786 10675 9682 19246 5454	59 20564 5216	51 3661 3039
9 19525 9485 7777 19999 8378 9209 3163 20232 6690 16518 716 7353	0 18226 17207	52 19010 18121
10 4588 6709 20202 10905 915 4317 11073 13576 16433 368 3508 21171	1 9380 8266	53 8968 11793
11 14072 4033 19959 12608 631 19494 14160 8249 10223 21504 12395 4322	2 7073 3065	54 13427 18003
12 13800 14161	3 18252 13437	55 5303 3083
13 2948 9647	4 9161 15642	56 531 16668
14 14693 16027	5 10714 10153	57 4771 6722
15 20506 11082	6 11585 9078	58 5695 7960
16 1143 9020	7 5359 9418	59 3589 14630
17 13501 4014	8 9024 9515	
18 1548 2190	9 1206 16354	
19 12216 21556	10 14994 1102	
20 2095 19897	11 9375 20796	
21 4189 7958	12 15964 6027	
22 15940 10048	13 14789 6452	
23 515 12614	14 8002 18591	
24 8501 8450	15 14742 14089	
25 17595 16784	16 253 3045	
26 5913 8495	17 1274 19286	
27 16394 10423	18 14777 2044	
28 7409 6981	19 13920 9900	
29 6678 15939	20 452 7374	
30 20344 12987	21 18206 9921	
31 2510 14588	22 6131 5414	
32 17918 6655	23 10077 9726	
33 6703 19451	24 12045 5479	
34 496 4217	25 4322 7990	
35 7290 5766	26 15616 5550	
36 10521 8925	27 15561 10661	
37 20379 11905	28 20718 7387	
38 4090 5838	29 2518 18804	
39 19082 17040	30 8984 2600	
40 20233 12352	31 6516 17909	
41 19365 19546	32 11148 98	
42 6249 19030	33 20559 3704	
43 11037 19193	34 7510 1569	
44 19760 11772	35 16000 11692	
45 19644 7428	36 9147 10303	
46 16076 3521	37 16650 191	
47 11779 21062	38 15577 18685	
48 13062 9682	39 17167 20917	
49 8934 5217	40 4256 3391	
50 11087 3319	41 20092 17219	
	42 9218 5056	

Table B4. (Rate 3/4)

0 6385 7901 14611 13389 11200 3252 5243 2504 2722 821 7374	7 15087 12138	15 6089 13084
1 11359 2698 357 13824 12772 7244 6752 15310 852 2001 11417	8 5053 6470	16 3938 2751
2 7862 7977 6321 13612 12197 14449 15137 13860 1708 6399 13444	9 12687 14932	17 8509 4648
3 1560 11804 6975 13292 3646 3812 8772 7306 5795 14327 7866	10 15458 1763	18 12204 8917
4 7626 11407 14599 9689 1628 2113 10809 9283 1230 15241 4870	11 8121 1721	19 5749 12443
5 1610 5699 15876 9446 12515 1400 6303 5411 14181 13925 7358	12 12431 549	20 12613 4431
6 4059 8836 3405 7853 7992 15336 5970 10368 10278 9675 4651	13 4129 7091	21 1344 4014
7 4441 3963 9153 2109 12683 7459 12030 12221 629 15212 406	14 1426 8415	22 8488 13850
8 6007 8411 5771 3497 543 14202 875 9186 6235 13908 3563	15 9783 7604	23 1730 14896
9 3232 6625 4795 546 9781 2071 7312 3399 7250 4932 12652	16 6295 11329	24 14942 7126
10 8820 10088 11090 7069 6585 13134 10158 7183 488 7455 9238	17 1409 12061	25 14983 8863
11 1903 10818 119 215 7558 11046 10615 11545 14784 7961 15619	18 8065 9087	26 6578 8564
12 3655 8736 4917 15874 5129 2134 15944 14768 7150 2692 1469	19 2918 8438	27 4947 396
13 8316 3820 505 8923 6757 806 7957 4216 15589 13244 2622	20 1293 14115	28 297 12805
14 14463 4852 15733 3041 11193 12860 13673 8152 6551 15108 8758	21 3922 13851	29 13878 6692
15 3149 11981	22 3851 4000	30 11857 11186
16 13416 6906	23 5865 1768	31 14395 11493
17 13098 13352	24 2655 14957	32 16145 12251
18 2009 14460	25 5565 6332	33 13462 7428
19 7207 4314	26 4303 12631	34 14526 13119
20 3312 3945	27 11653 12236	35 2535 11243
21 4418 6248	28 16025 7632	36 6465 12690
22 2669 13975	29 4655 14128	37 6872 9334
23 7571 9023	30 9584 13123	38 15371 14023
24 14172 2967	31 13987 9597	39 8101 10187
25 7271 7138	32 15409 12110	40 11963 4848
26 6135 13670	33 8754 15490	41 15125 6119
27 7490 14559	34 7416 15325	42 8051 14465
28 8657 2466	35 2909 15549	43 11139 5167
29 8599 12834	36 2995 8257	44 2883 14521
30 3470 3152	37 9406 4791	
31 13917 4365	38 11111 4854	
32 6024 13730	39 2812 8521	
33 10973 14182	40 8476 14717	
34 2464 13167	41 7820 15360	
35 5281 15049	42 1179 7939	
36 1103 1849	43 2357 8678	
37 2058 1069	44 7703 6216	
38 9654 6095	0 3477 7067	
39 14311 7667	1 3931 13845	
40 15617 8146	2 7675 12899	
41 4588 11218	3 1754 8187	
42 13660 6243	4 7785 1400	
43 8578 7874	5 9213 5891	
44 11741 2686	6 2494 7703	
0 1022 1264	7 2576 7902	
1 12604 9965	8 4821 15682	
2 8217 2707	9 10426 11935	
3 3156 11793	10 1810 904	
4 354 1514	11 11332 9264	
5 6978 14058	12 11312 3570	
6 7922 16079	13 14916 2650	
	14 7679 7842	

Table B5. (Rate 4/5)

0 149 11212 5575 6360 12559 8108 8505 408 10026 12828	16 920 1304	34 10377 8138
1 5237 490 10677 4998 3869 3734 3092 3509 7703 10305	17 1253 11934	35 7616 5811
2 8742 5553 2820 7085 12116 10485 564 7795 2972 2157	18 9559 6016	0 7285 9863
3 2699 4304 8350 712 2841 3250 4731 10105 517 7516	19 312 7589	1 7764 10867
4 12067 1351 11992 12191 11267 5161 537 6166 4246 2363	20 4439 4197	2 12343 9019
5 6828 7107 2127 3724 5743 11040 10756 4073 1011 3422	21 4002 9555	3 4414 8331
6 11259 1216 9526 1466 10816 940 3744 2815 11506 11573	22 12232 7779	4 3464 642
7 4549 11507 1118 1274 11751 5207 7854 12803 4047 6484	23 1494 8782	5 6960 2039
8 8430 4115 9440 413 4455 2262 7915 12402 8579 7052	24 10749 3969	6 786 3021
9 3885 9126 5665 4505 2343 253 4707 3742 4166 1556	25 4368 3479	7 710 2086
10 1704 8936 6775 8639 8179 7954 8234 7850 8883 8713	26 6316 5342	8 7423 5601
11 11716 4344 9087 11264 2274 8832 9147 11930 6054 5455	27 2455 3493	9 8120 4885
12 7323 3970 10329 2170 8262 3854 2087 12899 9497 11700	28 12157 7405	10 12385 11990
13 4418 1467 2490 5841 817 11453 533 11217 11962 5251	29 6598 11495	11 9739 10034
14 1541 4525 7976 3457 9536 7725 3788 2982 6307 5997	30 11805 4455	12 424 10162
15 11484 2739 4023 12107 6516 551 2572 6628 8150 9852	31 9625 2090	13 1347 7597
16 6070 1761 4627 6534 7913 3730 11866 1813 12306 8249	32 4731 2321	14 1450 112
17 12441 5489 8748 7837 7660 2102 11341 2936 6712 11977	33 3578 2608	15 7965 8478
18 10155 4210	34 8504 1849	16 8945 7397
19 1010 10483	35 4027 1151	17 6590 8316
20 8900 10250	0 5647 4935	18 6838 9011
21 10243 12278	1 4219 1870	19 6174 9410
22 7070 4397	2 10968 8054	20 255 113
23 12271 3887	3 6970 5447	21 6197 5835
24 11980 6836	4 3217 5638	22 12902 3844
25 9514 4356	5 8972 669	23 4377 3505
26 7137 10281	6 5618 12472	24 5478 8672
27 11881 2526	7 1457 1280	25 4453 2132
28 1969 11477	8 8868 3883	26 9724 1380
29 3044 10921	9 8866 1224	27 12131 11526
30 2236 8724	10 8371 5972	28 12323 9511
31 9104 6340	11 266 4405	29 8231 1752
32 7342 8582	12 3706 3244	30 497 9022
33 11675 10405	13 6039 5844	31 9288 3080
34 6467 12775	14 7200 3283	32 2481 7515
35 3186 12198	15 1502 11282	33 2696 268
0 9621 11445	16 12318 2202	34 4023 12341
1 7486 5611	17 4523 965	35 7108 5553
2 4319 4879	18 9587 7011	
3 2196 344	19 2552 2051	
4 7527 6650	20 12045 10306	
5 10693 2440	21 11070 5104	
6 6755 2706	22 6627 6906	
7 5144 5998	23 9889 2121	
8 11043 8033	24 829 9701	
9 4846 4435	25 2201 1819	
10 4157 9228	26 6689 12925	
11 12270 6562	27 2139 8757	
12 11954 7592	28 12004 5948	
13 7420 2592	29 8704 3191	
14 8810 9636	30 8171 10933	
	31 6297 7116	
	32 616 7146	
	33 5142 9761	
15 689 5430		

Table B6. (Rate 5/6)

0 4362 416 8909 4156 3216 3112 2560 2912 6405 8593 4969 6723	27 6174 5119	24 2303 646
1 2479 1786 8978 3011 4339 9313 6397 2957 7288 5484 6031 10217	28 7203 1989	25 2075 611
2 10175 9009 9889 3091 4985 7267 4092 8874 5671 2777 2189 8716	29 1781 5174	26 4687 362
3 9052 4795 3924 3370 10058 1128 9996 10165 9360 4297 434 5138	0 1464 3559	27 8684 9940
4 2379 7834 4835 2327 9843 804 329 8353 7167 3070 1528 7311	1 3376 4214	28 4830 2065
5 3435 7871 348 3693 1876 6585 10340 7144 5870 2084 4052 2780	2 7238 67	29 7038 1363
6 3917 3111 3476 1304 10331 5939 5199 1611 1991 699 8316 9960	3 10595 8831	0 1769 7837
7 6883 3237 1717 10752 7891 9764 4745 3888 10009 4176 4614 1567	4 1221 6513	1 3801 1689
8 10587 2195 1689 2968 5420 2580 2883 6496 111 6023 1024 4449	5 5300 4652	2 10070 2359
9 3786 8593 2074 3321 5057 1450 3840 5444 6572 3094 9892 1512	6 1429 9749	3 3667 9918
10 8548 1848 10372 4585 7313 6536 6379 1766 9462 2456 5606 9975	7 7878 5131	4 1914 6920
11 8204 10593 7935 3636 3882 394 5968 8561 2395 7289 9267 9978	8 4435 10284	5 4244 5669
12 7795 74 1633 9542 6867 7352 6417 7568 10623 725 2531 9115	9 6331 5507	6 10245 7821
13 7151 2482 4260 5003 10105 7419 9203 6691 8798 2092 8263 3755	10 6662 4941	7 7648 3944
14 3600 570 4527 200 9718 6771 1995 8902 5446 768 1103 6520	11 9614 10238	8 3310 5488
15 6304 7621	12 8400 8025	9 6346 9666
16 6498 9209	13 9156 5630	10 7088 6122
17 7293 6786	14 7067 8878	11 1291 7827
18 5950 1708	15 9027 3415	12 10592 8945
19 8521 1793	16 1690 3866	13 3609 7120
20 6174 7854	17 2854 8469	14 9168 9112
21 9773 1190	18 6206 630	15 6203 8052
22 9517 10268	19 363 5453	16 3330 2895
23 2181 9349	20 4125 7008	17 4264 10563
24 1949 5560	21 1612 6702	18 10556 6496
25 1556 555	22 9069 9226	19 8807 7645
26 8600 3827	23 5767 4060	20 1999 4530
27 5072 1057	24 3743 9237	21 9202 6818
28 7928 3542	25 7018 5572	22 3403 1734
29 3226 3762	26 8892 4536	23 2106 9023
0 7045 2420	27 853 6064	24 6881 3883
1 9645 2641	28 8069 5893	25 3895 2171
2 2774 2452	29 2051 2885	26 4062 6424
3 5331 2031	0 10691 3153	27 3755 9536
4 9400 7503	1 3602 4055	28 4683 2131
5 1850 2338	2 328 1717	29 7347 8027
6 10456 9774	3 2219 9299	
7 1692 9276	4 1939 7898	
8 10037 4038	5 617 206	
9 3964 338	6 8544 1374	
10 2640 5087	7 10676 3240	
11 858 3473	8 6672 9489	
12 5582 5683	9 3170 7457	
13 9523 916	10 7868 5731	
14 4107 1559	11 6121 10732	
15 4506 3491	12 4843 9132	
16 8191 4182	13 580 9591	
17 10192 6157	14 6267 9290	
18 5668 3305	15 3009 2268	
19 3449 1540	16 195 2419	
20 4766 2697	17 8016 1557	
21 4069 6675	18 1516 9195	
22 1117 1016	19 8062 9064	
23 5619 3085	20 2095 8968	
24 8483 8400	21 753 7326	
25 8255 394	22 6291 3833	
26 6338 5042	23 2614 7844	

Table B7. (Rate 8/9)

0 6235 2848 3222	13 1969 3869	7 6356 4756	1 2062 6599	15 5306 478
1 5800 3492 5348	14 3571 2420	8 3930 418	2 4597 4870	16 4320 6121
2 2757 927 90	15 4632 981	9 211 3094	3 1228 6913	17 3961 1125
3 6961 4516 4739	16 3215 4163	10 1007 4928	4 4159 1037	18 5699 1195
4 1172 3237 6264	17 973 3117	11 3584 1235	5 2916 2362	19 6511 792
5 1927 2425 3683	18 3802 6198	12 6982 2869	6 395 1226	0 3934 2778
6 3714 6309 2495	19 3794 3948	13 1612 1013	7 6911 4548	1 3238 6587
7 3070 6342 7154	0 3196 6126	14 953 4964	8 4618 2241	2 1111 6596
8 2428 613 3761	1 573 1909	15 4555 4410	9 4120 4280	3 1457 6226
9 2906 264 5927	2 850 4034	16 4925 4842	10 5825 474	4 1446 3885
10 1716 1950 4273	3 5622 1601	17 5778 600	11 2154 5558	5 3907 4043
11 4613 6179 3491	4 6005 524	18 6509 2417	12 3793 5471	6 6839 2873
12 4865 3286 6005	5 5251 5783	19 1260 4903	13 5707 1595	7 1733 5615
13 1343 5923 3529	6 172 2032	0 3369 3031	14 1403 325	8 5202 4269
14 4589 4035 2132	7 1875 2475	1 3557 3224	15 6601 5183	9 3024 4722
15 1579 3920 6737	8 497 1291	2 3028 583	16 6369 4569	10 5445 6372
16 1644 1191 5998	9 2566 3430	3 3258 440	17 4846 896	11 370 1828
17 1482 2381 4620	10 1249 740	4 6226 6655	18 7092 6184	12 4695 1600
18 6791 6014 6596	11 2944 1948	5 4895 1094	19 6764 7127	13 680 2074
19 2738 5918 3786	12 6528 2899	6 1481 6847	0 6358 1951	14 1801 6690
0 5156 6166	13 2243 3616	7 4433 1932	1 3117 6960	15 2669 1377
1 1504 4356	14 867 3733	8 2107 1649	2 2710 7062	16 2463 1681
2 130 1904	15 1374 4702	9 2119 2065	3 1133 3604	17 5972 5171
3 6027 3187	16 4698 2285	10 4003 6388	4 3694 657	18 5728 4284
4 6718 759	17 4760 3917	11 6720 3622	5 1355 110	19 1696 1459
5 6240 2870	18 1859 4058	12 3694 4521	6 3329 6736	
6 2343 1311	19 6141 3527	13 1164 7050	7 2505 3407	
7 1039 5465	0 2148 5066	14 1965 3613	8 2462 4806	
8 6617 2513	1 1306 145	15 4331 66	9 4216 214	
9 1588 5222	2 2319 871	16 2970 1796	10 5348 5619	
10 6561 535	3 3463 1061	17 4652 3218	11 6627 6243	
11 4765 2054	4 5554 6647	18 1762 4777	12 2644 5073	
12 5966 6892	5 5837 339	19 5736 1399	13 4212 5088	
	6 5821 4932	0 970 2572	14 3463 3889	

Table B8. (Rate 9/10)

0 5611 2563 2900	14 4289 1846	12 3540 375	10 3710 3853	8 1303 3496
1 5220 3143 4813	15 5374 6208	13 191 2782	11 5123 931	9 3516 3639
2 2481 834 81	16 1775 3476	14 906 4432	12 6146 3323	10 5161 2293
3 6265 4064 4265	17 3216 2178	15 3225 1111	13 1939 5002	11 4682 3845
4 1055 2914 5638	0 4165 884	16 6296 2583	14 5140 1437	12 3045 643
5 1734 2182 3315	1 2896 3744	17 1457 903	15 1263 293	13 2818 2616
6 3342 5678 2246	2 874 2801	0 855 4475	16 5949 4665	14 3267 649
7 2185 552 3385	3 3423 5579	1 4097 3970	17 4548 6380	15 6236 593
8 2615 236 5334	4 3404 3552	2 4433 4361	0 3171 4690	16 646 2948
9 1546 1755 3846	5 2876 5515	3 5198 541	1 5204 2114	17 4213 1442
10 4154 5561 3142	6 516 1719	4 1146 4426	2 6384 5565	0 5779 1596
11 4382 2957 5400	7 765 3631	5 3202 2902	3 5722 1757	1 2403 1237
12 1209 5329 3179	8 5059 1441	6 2724 525	4 2805 6264	2 2217 1514
13 1421 3528 6063	9 5629 598	7 1083 4124	5 1202 2616	3 5609 716
14 1480 1072 5398	10 5405 473	8 2326 6003	6 1018 3244	4 5155 3858
15 3843 1777 4369	11 4724 5210	9 5605 5990	7 4018 5289	5 1517 1312
16 1334 2145 4163	12 155 1832	10 4376 1579	8 2257 3067	6 2554 3158
17 2368 5055 260	13 1689 2229	11 4407 984	9 2483 3073	7 5280 2643
0 6118 5405	14 449 1164	12 1332 6163	10 1196 5329	8 4990 1353
1 2994 4370	15 2308 3088	13 5359 3975	11 649 3918	9 5648 1170
2 3405 1669	16 1122 669	14 1907 1854	12 3791 4581	10 1152 4366
3 4640 5550	17 2268 5758	15 3601 5748	13 5028 3803	11 3561 5368
4 1354 3921	0 5878 2609	16 6056 3266	14 3119 3506	12 3581 1411
5 117 1713	1 782 3359	17 3322 4085	15 4779 431	13 5647 4661
6 5425 2866	2 1231 4231	0 1768 3244	16 3888 5510	14 1542 5401
7 6047 683	3 4225 2052	1 2149 144	17 4387 4084	15 5078 2687
8 5616 2582	4 4286 3517	2 1589 4291	0 5836 1692	16 316 1755
9 2108 1179	5 5531 3184	3 5154 1252	1 5126 1078	17 3392 1991
10 933 4921	6 1935 4560	4 1855 5939	2 5721 6165	
11 5953 2261	7 1174 131	5 4820 2706	3 3540 2499	
12 1430 4699	8 3115 956	6 1475 3360	4 2225 6348	
13 5905 480	9 3129 1088	7 4266 693	5 1044 1484	
	10 5238 4440	8 4156 2018	6 6323 4042	
	11 5722 4280	9 2103 752	7 1313 5603	

Annex C (normative): Addresses of parity bit accumulators for $n_{ldpc}=16200$

Table C1. Rate 1/2

15 5604 5754 7705 4356 6844 8186 4014	24 5140 2003 1263 4742 6497 1185 6202	8 4606 3080
16 5341 2456 6053 4571 5034 8521 1858	0 4046 6934	9 4633 7877
17 5207 8819 4926 8482 7518 8225 2585	1 2855 66	10 3884 6868
18 4948 1285 6825 8840 3454 8255 3137	2 6694 212	11 8935 4996
19 672 263 6959 5970 2556 1273 6091	3 3439 1158	12 3028 764
20 712 2386 6354 4061 1062 5045 5158	4 3850 4422	13 5988 1057
21 2543 5748 4822 2348 3089 6328 5876	5 5924 290	14 7411 3450
22 926 5701 269 3693 2438 3190 3507	6 1467 4049	
23 2802 4520 3577 5324 1091 4667 4449	7 7820 2242	

Table C2. Rate 3/5

2765 5713 6426 3596 1374 4811 2182	5516 1622 2906 3285 1257 5797 3816	7 4292 956
544 3394 2840 4310 771	817 875 2311 3543 1205	8 5692 3417
4951 211 2208 723 1246 2928 398 5739	4244 2184 5415 1705 5642 4886 2333	9 266 4878
265 5601 5993 2615	287 1848 1121 3595 6022	10 4913 3247
210 4730 5777 3096 4282 6238 4939	2142 2830 4069 5654 1295 2951 3919	11 4763 3937
1119 6463 5298 6320 4016	1356 884 1786 396 4738	12 3590 2903
4167 2063 4757 3157 5664 3956 6045	0 2161 2653	13 2566 4215
563 4284 2441 3412 6334	1 1380 1461	14 5208 4707
4201 2428 4474 59 1721 736 2997 428	2 2502 3707	15 3940 3388
3807 1513 4732 6195	3 3971 1057	16 5109 4556
2670 3081 5139 3736 1999 5889 4362	4 5985 6062	17 4908 4177
3806 4534 5409 6384 5809	5 1733 6028	
	6 3786 1936	

Table C3. Rate 2/3

0 2084 1613 1548 1286 1460 3196 4297	8 3011 1436	5 2155 2922
2481 3369 3451 4620 2622	9 2167 2512	6 347 2696
1 122 1516 3448 2880 1407 1847 3799	10 4606 1003	7 226 4296
3529 373 971 4358 3108	11 2835 705	8 1560 487
2 259 3399 929 2650 864 3996 3833 107	12 3426 2365	9 3926 1640
5287 164 3125 2350	13 3848 2474	10 149 2928
3 342 3529	14 1360 1743	11 2364 563
4 4198 2147	0 163 2536	12 635 688
5 1880 4836	1 2583 1180	13 231 1684
6 3864 4910	2 1542 509	14 1129 3894
7 243 1542	3 4418 1005	
	4 5212 5117	

Table C4. Rate 3/4

0 3576 1576 3860 1290 4199 815 2978 3428 3639 2181 1750	0 1458 3031	0 2480 3079
1 1960 2307 2697 4240 3238 3555 265 379 128 2911 3653	1 3003 1328	1 3021 1088
2 99 1389 3627 830 2448 1185 3034 2946 2598 1960 1032	2 1137 1716	2 713 1379
3 3198 478 4207 1481 1009 2616 1924 3437 554 683 1801	3 132 3725	3 997 3903
4 2681 2135	4 1817 638	4 2323 3361
5 3107 4027	5 1774 3447	5 1110 986
6 2637 3373	6 3632 1257	6 2532 142
7 3830 3449	7 542 3694	7 1690 2405
8 4129 2060	8 1015 1945	8 1298 1881
9 4184 2742	9 1948 412	9 615 174
10 3946 1070	10 995 2238	10 1648 3112
11 2239 984	11 4141 1907	11 1415 2808

Table C5. Rate 4/5

	0 2272 1197
	1 1800 3280
0 2319 198 789 902 1314 2806 143 2088 3525 1972	2 331 2308
1 1285 1816 2194 1037 3293 509 3417 2294 2438 3111	3 465 2552
2 704 1967 1228 1486 842 3400 1075 2776 3473 3327	4 1038 2479
3 1501 63 3235 2253 661 2968 1819 252 360 2174	5 1383 343
4 3040 2231 2531 2690 1527 2605 2130 791 1786 1699	6 94 236
5 896 1565	7 2619 121
6 2493 184	8 1497 2774
7 212 3210	9 2116 1855
8 727 1339	0 722 1584
9 3428 612	1 2767 1881
0 2663 1947	2 2701 1610
1 230 2695	3 3283 1732
2 2025 2794	4 168 1099
3 3039 283	5 3074 243
4 862 2889	6 3460 945
5 376 2110	7 2049 1746
6 2034 2286	8 566 1427
7 951 2068	9 3545 1168
8 3108 3542	
9 307 1421	

Table C6. Rate 5/6

	3 1914 2831
	4 532 1450
0 1752 825 2637 402 2730 1838 1945 2490 1627 2137 1202 2188	5 91 974
1 1501 1900 2147 1967 1757 2803 555 2020 333 2266 2577 1399	6 497 2228
2 1675 799 422 488 945 1536 2288 999 1727 2214 1923 2152	7 2326 1579
3 2409 499 1481 908 559 716 1270 333 2508 2264 1702 2805	0 2482 256
4 2447 1926	1 1117 1261
5 414 1224	2 1257 1658
6 2114 842	3 1478 1225
7 212 573	4 2511 980
0 2383 2112	5 2320 2675
1 2286 2348	6 435 1278
2 545 819	7 228 503
3 1264 143	0 1885 2369
4 1701 2258	1 57 483
5 964 166	2 838 1050
6 114 2413	3 1231 1990
7 2243 81	4 1738 68
0 1245 1581	5 2392 951
1 775 169	6 163 645
2 1696 1104	7 2644 1704

Table C7. Rate 8/9

0 1558 712 805	4 1496 502	3 544 1190
1 1450 873 1337	0 1006 1701	4 1472 1246
2 1741 1129 1184	1 1155 97	0 508 630
3 294 806 1566	2 657 1403	1 421 1704
4 482 605 923	3 1453 624	2 284 898
0 926 1578	4 429 1495	3 392 577
1 777 1374	0 809 385	4 1155 556
2 608 151	1 367 151	0 631 1000
3 1195 210	2 1323 202	1 732 1368
4 1484 692	3 960 318	2 1328 329
0 427 488	4 1451 1039	3 1515 506
1 828 1124	0 1098 1722	4 1104 1172
2 874 1366	1 1015 1428	
3 1500 835	2 1261 1564	

Annex D: Additional Mode Adaptation and ACM tools

D1 “ACM Command” signalling interface

“ACM Command” signalling input (see **Figure 1**) shall allow setting, by an external “transmission mode control unit”, of the transmission parameters to be adopted by the DVB-S2 modulator, for a specific portion of input data.

“ACM Command” shall carry the following signals:

- MODCOD (5 signals in parallel, according to **Table 12**)
- CVALID (Command Valid)
- SEND

For Generic Input Stream, the ACM modulator shall apply the specified MODCOD to user data included in the interval from CVALID and SEND, and shall deliver such user data immediately. The user data included in the interval between CVALID=active and SEND=active shall not exceed the capacity of $(K_{bch}-80)$ bits, K_{bch} being the transmittable bits associated with a specific MODCOD for $n_{ldpc}=64800$. The modulator shall transmit one (or more) short FECFRAME(s) instead of one normal FECFRAME when this is advantageous in terms of transmission efficiency.

For input Transport Streams, ACM is implemented via null-packet deletion function, therefore input user data do not correspond directly to the transmitted data. In this case, the SEND function not active. The ACM modulator shall continuously apply the specified MODCOD to user data after CVALID=active.

An example temporisation of ACM Command is given in **Figure D1-1**

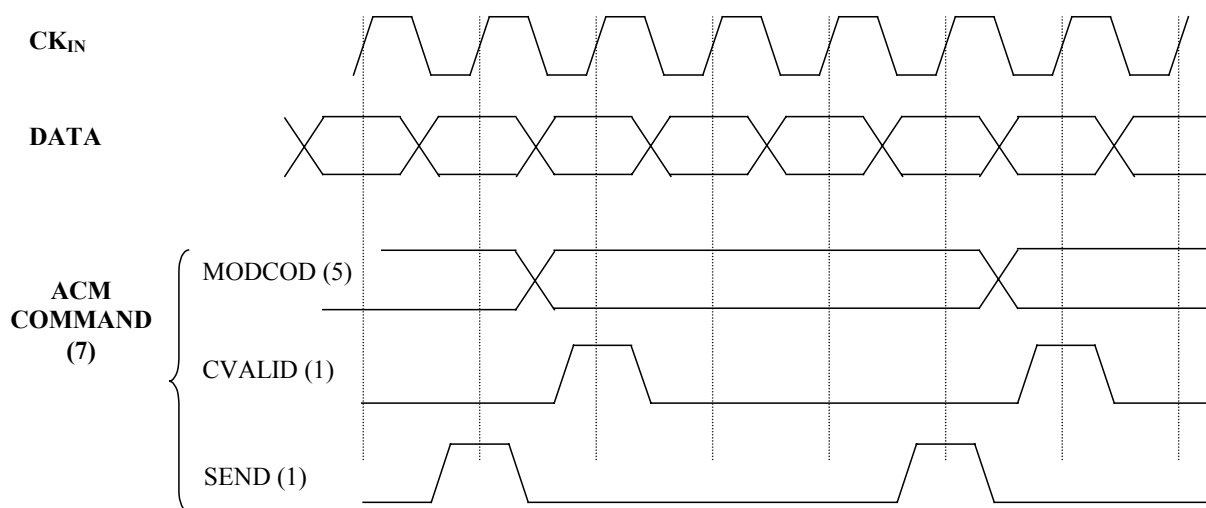


Figure D1-1: Example temporisation of ACM Command

D2 Input Stream Synchroniser (optional)

Delays and packet jitter introduced by DVB-S2 modems may depend from the transmitted bit-rate and may change in time during ACM rate switching. The “Input Stream Synchroniser” (see **Figure 1** and **D2-1**) shall provide an optional mechanism to regenerate, in the receiver, the clock of the Transport Stream (or packetised Generic Stream) at the modulator Mode Adapter input, in order to guarantee end-to-end constant bit rates and delays (see also **Annex F, Figure F3.1**, example receiver implementation).

When ISSYI=1 in MATYPE field (see **Table 3**), a counter shall be activated (22 bits), clocked by the modulator symbol rate (frequency R_s). The Input Stream SYNchronisation field (ISSY, 2 or 3 bytes) shall be appended after each input packet (in the case of Transport Streams, before null-packet deletion takes place), as shown in **Figure D2-1**. ISSY shall be coded according to **Table D2-1**, sending the following variables:

- **ISCR** (short: 15 bits; long:22 bits) (ISCR=Input Stream Time Reference), loaded with the LSBs of the counter content at the instant the relevant input packet is processed (at constant rate R_{IN})
- **BUFS** (2+10 bits) (BUFS= maximum size of the requested receiver buffer to compensate delay variations). If ISSYI=1, this variable shall be transmitted at least 5 times per second, replacing ISCR. The maximum buffer size required in the receiver shall be [20 Mbit].
- **BUFSTAT** (2+10 bits) (BUFSTAT = actual status to reset the receiver buffer=number of filled bits). If ISSYI=1, this variable shall be transmitted at least 5 times per second, replacing ISCR.

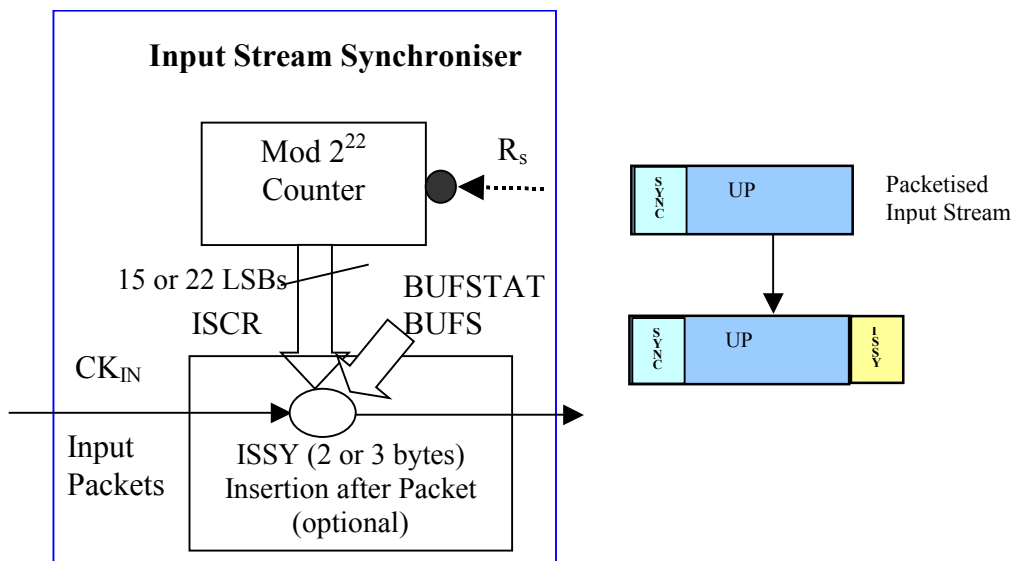


Figure D2-1: Input Stream Synchroniser block diagram

Table D2-1 – ISSY field coding (2 or 3 bytes)						
First Byte					Second Byte	Third Byte
bit-7 (MSB)	bit-6	bit-5 and bit-4	bit-3 and bit-2	bit-1 and bit-0	bit-7 to bit-0	bit-7 bit-0
0=ISCR _{short}	MSB of ISCR _{short}	next 6 bits of ISCR _{short}			next 8 bits of SCR _{short}	not present
1	0=ISCR _{long}	6 MSBs of ISCR _{long}			next 8 bits of ISCR _{long}	next 8 bits of ISCR _{long}
1	1	00=BUFS	BUFS unit 00=bits 01=Kbits 10=Mbits 11=reserved	4 MSBs of BUFS	next 6 bits of BUFS	not present when ISCR _{short} is used; else reserved
1	1	10=BUFSTAT	BUFSTAT unit 00=bits 01=Kbits 10=Mbits 11=reserved	4 MSBs of BUFSTAT	next 6 bits of BUFSTAT	not present when ISCR _{short} is used; else reserved
1	1	others=reserved	reserved	reserved	reserved	not present when ISCR _{short} is used; else reserved

An example receiver scheme to regenerate the output packet stream and the relevant clock R'_{IN} is given in **Figure F3.1**.

D3 Null-packet Deletion (Normative for Input Transport Streams and ACM)

Transport Stream rules require that the bit rates at the output of the MUX and the input of the DEMUX are constant in time, and the end-to-end delay is also constant. In order to fulfil such requirements in an ACM environment, the null-packet deletion function shall be activated (see Annex G for application examples).

As shown in **Figure D3-1**, Useful Packets (i.e. packets with $PID \neq 8191_D$) (including the optional ISSY appended field) shall be transmitted while null-packets ($PID = 8191_D$) (including the optional ISSY appended field) shall be removed.

After transmission of a UP, a counter called DNP (Deleted Null-Packets, 1 byte) shall be first reset and then incremented at each deleted null-packet. The counter content shall be appended after the Least Significant Byte of the next transmitted useful packet, then DNP shall be reset. When DNP reaches the maximum allowed value $DNP = 255_D$, then if the following packet is again a null-packet this null-packet is kept as a useful packet and transmitted.

The resulting stream has $UPL = (188 + 1) \times 8$ bits (for $ISSYI = 0$) or $UPL = (188 + 2 + 1) \times 8$ bits (for $ISSYI = 1$ and $ISCR_{short}$), or $UPL = (188 + 3 + 1) \times 8$ bits (for $ISSYI = 1$ and $ISCR_{long}$), since the Transport Stream packets are extended by the DNP and ISSY (optional) fields.

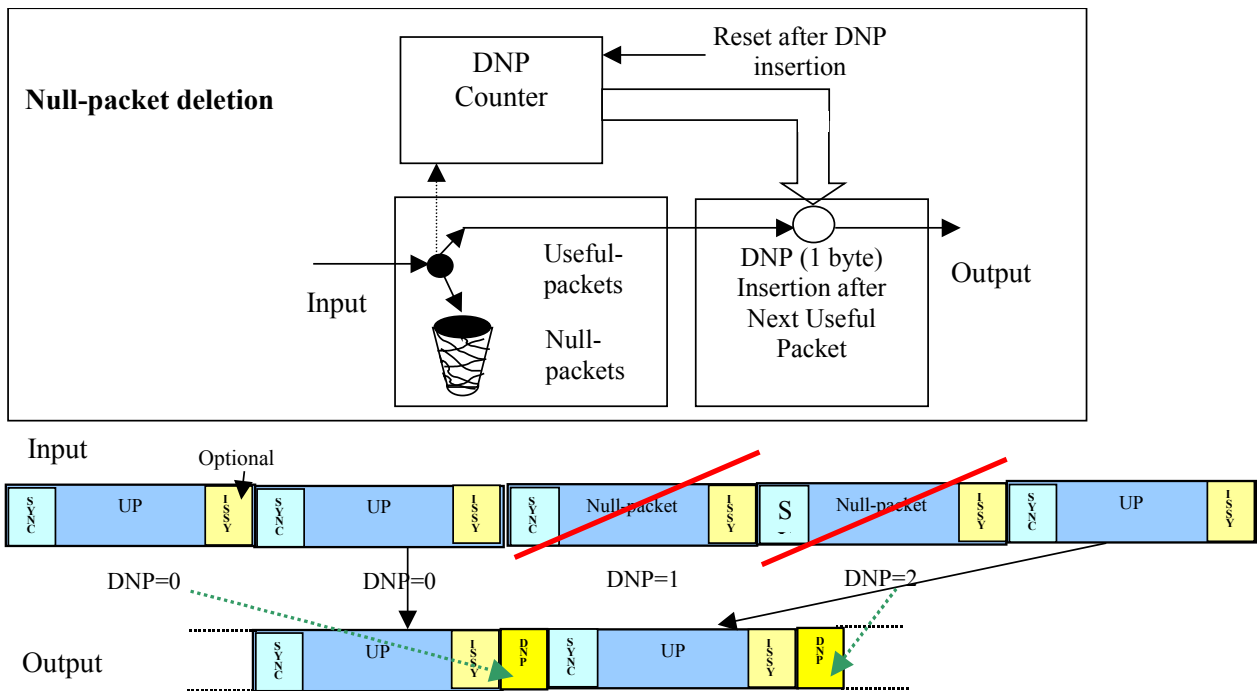


Figure D3-1: Null-packet deletion and DNP field (1 byte) insertion

D4 BBHEADER and Slicing Policy for various application areas

According to the application area, BBHeader coding and Slicing policy shall be according to **Table D4-1**.

Table D4-1: BBHeader coding for various application areas and Slicing policy								
Application area / configuration	MATYPE-1	MATY PE-2	UPL	DFL	SYNC	SYNCD	CRC-8	Slicing policy
Broadcasting / CCM, single TS	111100Y	X	188 _D x8	k _{BCH} – 80 _D	47 _{HEX}	Y	Y	Break No timeout No Padding No Dummy
Broadcasting, differentiated protection level per stream/ VCM, constant protection level per TS, Multiple TS	1100Y0Y	Y	188 _D x8 (+16 or 24 if ISSYI=1)	k _{BCH} – 80 _D	47 _{HEX}	Y	Y	Break Read (1) No timeout No Padding Yes Dummy
DSNG with time variable protection level / ACM, single TS input, NP-deletion, ACM Command active	111011Y	X	189 _D x8+ (16 or 24)	k _{BCH} – 80 _D	47 _{HEX}	Y	Y	Break Read (0) No timeout No Padding Yes Dummy
Interactive services with ACM over TS, differentiated protection per stream / ACM, constant protection level per TS, Multiple TS, NP- deletion	1100Y1Y	Y	189 _D x8 (+16 or 24 if ISSYI=1)	Y ≤k _{BCH} – 80 _D	47 _{HEX}	Y	Y	Read(1) or (2) Yes Padding Yes Dummy YES shortframe
Interactive services (IP) with ACM over GS, differentiated protection per stream / ACM, constant protection level per input stream, Multiple Generic Stream	010000Y	Y	0	Y ≤k _{BCH} – 80 _D	X	X	Y	Read(1) or (2) Yes Padding Yes Dummy YES shortframe
Interactive services (IP) with ACM over GS, time variable protection / ACM, time variable protection level, Single Generic Stream, ACM Command active	011000Y	X	0	Y ≤k _{BCH} – 80 _D	X	X	Y	According to ACM Command Yes Padding Yes Dummy YES shortframe
X= not defined; Y=according to configuration/computation Break= break packets in subsequent DATAFIELDS; Timeout: maximum delay in merger/slicer buffer Read(0)= Read [k _{BCH} (Normal FECFRAME)– 80] bits when available, otherwise dummy Read(1)= Round-robin polling. Read [k _{BCH} (Normal FECFRAME)– 80] bits from port i when available, otherwise poll the next port Read (2)= On timeout, read DFL bits from port i and select the shortest FECFRAME containing DFL								

D5 Signalling of reception quality via return channel (Normative for ACM)

In ACM modes, the receiver shall signal the reception quality via an available return channel, according to the various DVB interactive systems, such as for example DVB-RCS (EN 301 790), DVB-RCP (ETS 300801), DVB-RCG (EN 301195), DVB-RCC (ES 200800). DVB “Network Independent Protocols for DVB Interactive Services” (ETS 300 802v1) may be adopted to achieve maximum network interoperability. Other simpler or optimised solutions (e.g. to guarantee minimum signalling delay) may be adopted to directly interface with the aforementioned DVB interactive systems.

The receiver shall evaluate quality-of-reception parameters, in particular:

- carrier to noise+ interference ratio in dB available at the receiver, indicated as **CNI**.

CNI format shall be:

$$\text{CNI} = 20 + 10 \{10 \text{Log}_{10}[\text{C}/(\text{N}+\text{I})]\} \quad (\text{positive integer in the range 0 to 240}).$$

In fact for DVB-S2 $10 \text{Log}_{10}[\text{C}/(\text{N}+\text{I})]$ may be in the range -2 dB to $+24$ dB.

$10 \text{Log}_{10}[\text{C}/(\text{N}+\text{I})]$ shall be evaluated with a quantised accuracy better than 1 dB (accuracy = mean error + 3σ , where σ is the standard deviation). Since modulation and coding modes for DVB-S2 are typically spaced 1-1,5 dB apart, a quantised precision better than 0.3 dB is recommended in order to fully exploit system capabilities. The measurement process is assumed to be continuous. A possible method to evaluate CNI is by using symbols known a-priori at the receiver, such as those in the SOF field of the PLFRAME Header and, when available, pilot symbols.

CNI and other optional reception quality parameters (such as for example the BER on the channel evaluated by counting the errors corrected by the LDPC decoder, the packet error rate detected by CRC-8, the CNI distance from the QeF threshold) may optionally be used by the receiver to identify the maximum throughput DVB-S2 transmission mode that it may decode at QeF, indicated by MODCOD_RQ and coded according to **Table 12**.

As a minimum, the CNI and MODCOD_RQ parameters shall be sent to the satellite network operator Gateway every time the protection on the DVB-S2 channel has to be changed. The maximum delay required for CNI and MODCOD evaluation and delivery to the Gateway via the interaction channel shall be no more than 300 ms, but this delay should be minimised if services interruptions are to be avoided under fast fading conditions ($\text{C}/\text{N}+\text{I}$ variations as fast as 0,5 to 1 dB/s may occur in Ka band). Optionally the gateway may acknowledge the reception of the message and the execution of the command by a message containing the new adopted MODCOD, coded according to **Table 12**. MODCOD= \langle MODCOD_RQ, since the allocated protection shall be equal or more robust than that requested by the terminal.

Example Transmission Protocol using (ETS 300 802v1)

DVBS2_Change_Modcod message shall be sent from the receiving terminal to the satellite network operator gateway, every time the protection on the DVB-S2 channel has to be changed.

DVBS2_Change_Modcod()	length in bits (big-endian notation)
{	
CNI;	32
MODCOD_RQ;	8
}	

DVBS2_Ack_Modcod message shall optionally be sent from the Gateway to the receiving terminal to acknowledge the DVB-S2 protection level modification.

DVBS2_Ack_Modcod()	length in bits (big-endian notation)
{	
MODCOD;	8
}	

Annex E: (Normative) SI Implementation for DSNG and other contribution applications

In DSNG transmissions, editing of the SI tables in the field may be impossible due to operational problems. Therefore, only the following MPEG2-defined SI tables PAT, PMT and Transport Stream Descriptor Table (TSDT) are mandatory. DSNG transmission using DVB-S2 shall implement SI according to **Annex D** (titled: “SI Implementation for DSNG and other contribution applications”) of EN 301 210, “DVB: Framing structure, channel coding and modulation for Digital Satellite News Gathering (DSNG) and other contribution applications by satellite”.

Annex F: (Informative) Supplementary information on receiver implementation

Receiver specification is not under the scope of this EN. This section includes some tutorial material on receiver implementation, although other techniques may be used offering the target functionalities and receiver performance.

F1. Carrier and clock recovery

To be completed.

F2. FEC decoding

LDPC codes are linear block codes with sparse parity check matrices $H_{(n-k) \times n}$.

As an example, an LDPC code of length $N=8$ and rate $\frac{1}{2}$ can be specified by the following parity check matrix.

$$H = \begin{matrix} & \begin{matrix} n_1 & n_2 & n_3 & n_4 & n_5 & n_6 & n_7 & n_8 \end{matrix} \\ \begin{matrix} m_1 \\ m_2 \\ m_3 \\ m_4 \end{matrix} & \begin{bmatrix} 1 & 0 & 0 & 1 & 1 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 1 & 0 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 & 1 & 0 \end{bmatrix} \end{matrix}$$

The same code can be equivalently represented by the bipartite graph in **Figure 2.1a** which connects each check equation (check node) to its participating bits (bit nodes).

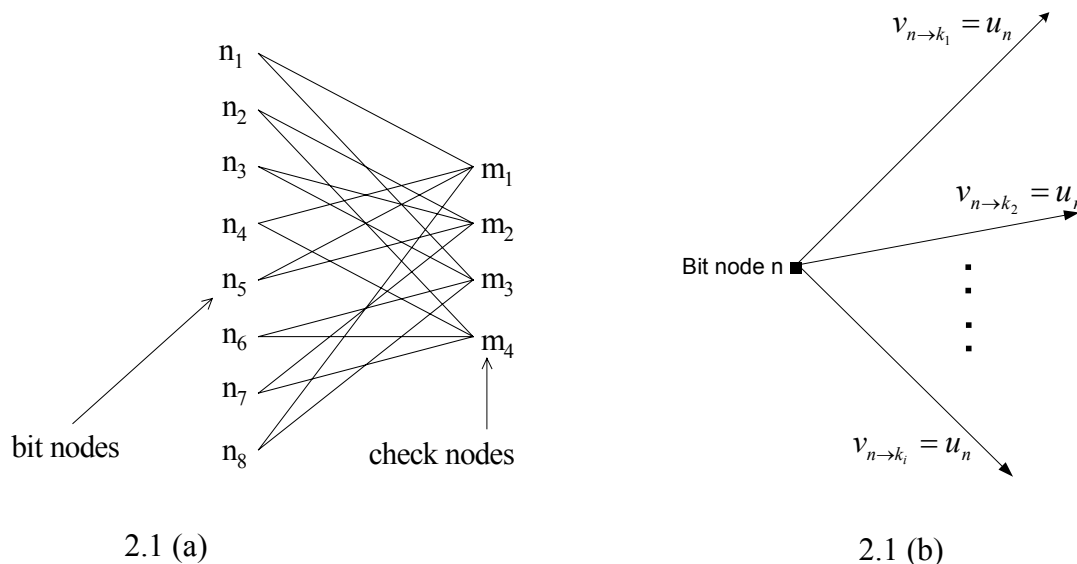


Figure F2.1. (a) Bipartite graph of an LDPC code; (b) Initialisation of outgoing messages from bit nodes

The purpose of the decoder is to determine the transmitted values of the bits. Bit nodes and check nodes communicate with each other to accomplish that. The decoding starts by assigning the channel values to the outgoing edges from bit nodes to check nodes. Upon receiving that, the check nodes make use of the parity check equations to update the bit node information and sends it back. Each bit node then performs a soft majority vote among the information reaching him. At this point, if the hard decisions on the bits satisfy all of the parity check equations, it means a valid codeword has been found and the process stops. Otherwise bit nodes go on sending the result of their soft majority votes to the check nodes. In the following sections, we describe the decoding algorithm in detail. The number of edges adjacent to a node is called the degree of that node.

Initialization:

$$v_{n \rightarrow k_i} = u_n, \quad n = 0, 1, \dots, N-1, \quad i = 1, 2, \dots, \text{deg}(\text{bit node } n)$$

Here $v_{n \rightarrow k_i}$ denotes the message that goes from bit node n to its adjacent check node k_i ,

u_n denotes the channel value for the bit n and N is the codeword size. The initialization process is also shown in **Figure F2.1b**.

Check node update:

Let us denote the incoming messages to the check node k from its d_c adjacent bit nodes by $v_{n_1 \rightarrow k}, v_{n_2 \rightarrow k}, \dots, v_{n_{d_c} \rightarrow k}$ (see **Figure F2.2a**). Our aim is to compute the outgoing messages from the check node k back to d_c adjacent bit nodes. Let us denote these messages by $w_{k \rightarrow n_1}, w_{k \rightarrow n_2}, \dots, w_{k \rightarrow n_{d_c}}$.

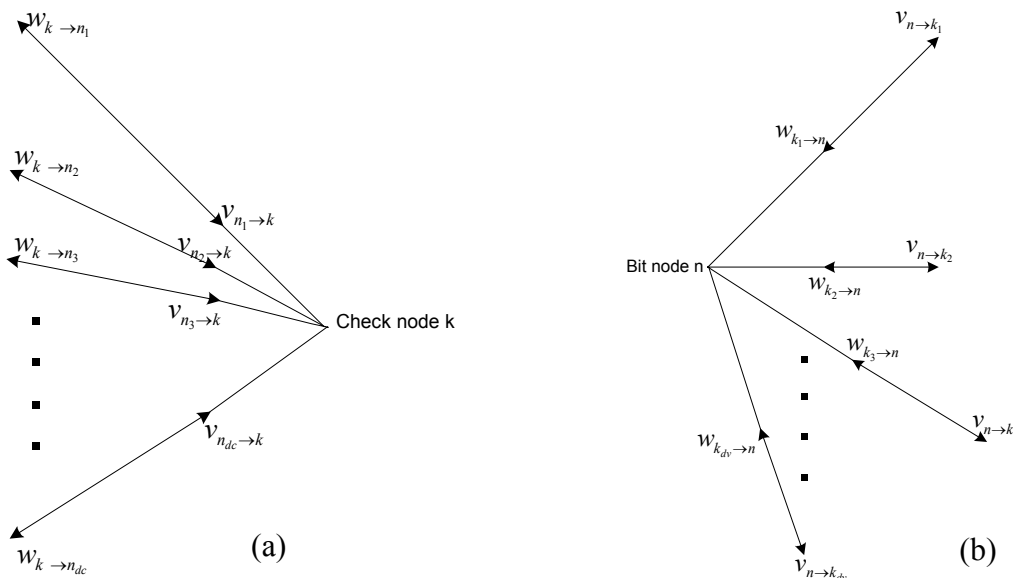


Figure F2.2 Message update at check nodes (a), and at bit nodes (b)

$$w_{k \rightarrow n_i} = g(v_{n_1 \rightarrow k}, v_{n_2 \rightarrow k}, \dots, v_{n_{i-1} \rightarrow k}, v_{n_{i+1} \rightarrow k}, \dots, v_{n_{d_c} \rightarrow k})$$

where $g(a, b) = \text{sign}(a) \times \text{sign}(b) \times \{\min(|a|, |b|)\} + LUT_g(a, b)$

Bit Node Update:

Let us denote the incoming messages to the bit node n from its d_v adjacent check nodes by $w_{k_1 \rightarrow n}, w_{k_2 \rightarrow n}, \dots, w_{k_{d_v} \rightarrow n}$ (see Figure F2.2b). Our aim is to compute the outgoing messages from the bit node n back to d_v adjacent check nodes. Let us denote these messages by $v_{n \rightarrow k_1}, v_{n \rightarrow k_2}, \dots, v_{n \rightarrow k_{d_v}}$.

They are computed as follows:
$$v_{n \rightarrow k_i} = u_n + \sum_{j \neq i} w_{k_j \rightarrow n}$$

Hard Decision Making:

After the bit node updates, hard decision can be made for each bit n by looking at the sign of $v_{n \rightarrow k_i} + w_{k_i \rightarrow n}$ for any k_i . If the hard decisions satisfy all the parity check equations, it means a valid codeword has been found, therefore the process stops. Otherwise another check node/bit node update is performed. If no convergence is achieved after a pre-determined number of iterations, the current output is given out. As SNR increases, the decoder converges with fewer iterations.

F3. ACM: Transport Stream regeneration and clock recovery using ISCR

When the modulator operates in ACM mode (null-packet deletion active), the receiver may regenerate the Transport Stream by inserting, before each useful packet, DNP null-packets in the reception FIFO buffer. As shown in **Figure F3.1**, the Transport Stream clock R'_{IN} may be recovered by means of a phase locked loop (PLL). The recovered symbol-rate R_S may be used to clock a local counter (which by definition runs synchronously with the input stream synchronisation counter of **Figure D1**, apart from the Satellite Doppler frequency shift). The PLL compares the local counter content with the transmitted ISCR of each TS packet, and the phase difference may be used to adjust the R'_{IN} clock. In this way R'_{IN} remains constant, and the reception FIFO buffer automatically compensates the chain delay variations. Since the reception FIFO buffer is not self-balancing, the BUFSTAT and the BUFS information may be used to set its initial state.

As an alternative, when dynamic variations of the end-to-end delay and bit-rate may be acceptable by the source decoders, the receiver buffer filling condition may be used to drive the PLL. In this case the reception buffer is self-balancing (in steady state half of cells are filled), and the ISSY field may be omitted at the transmitting side.

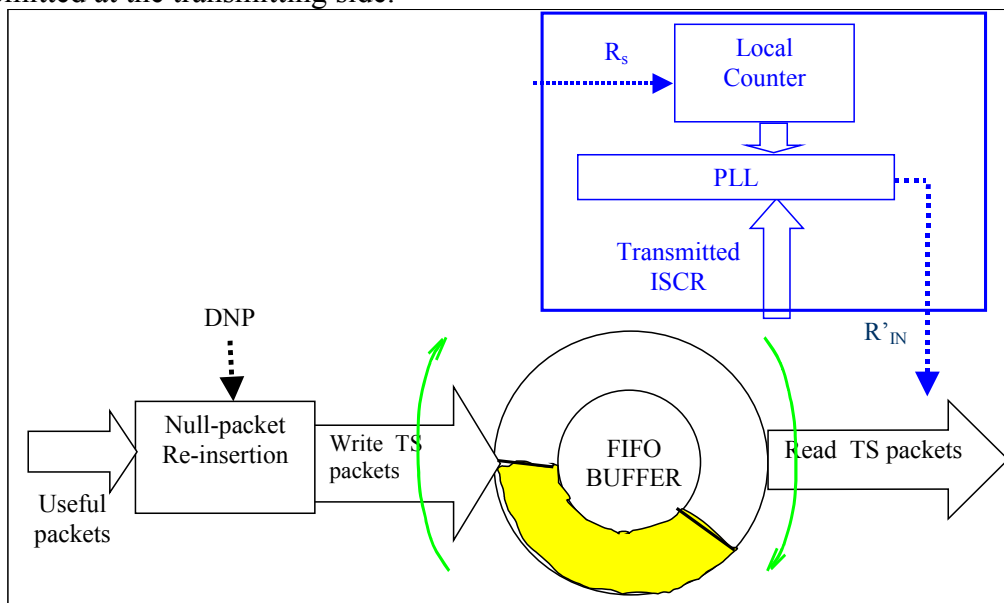


Figure F3.1. Example receiver block diagram for Null-packet re-insertion and R_{TS} clock recovery

Annex G (informative): Examples of possible use of the System*G1. Constant Coding and Modulation (digital TV broadcasting): bit rate capacity and C/N requirements*

The DVB-S2 system may be used in “single carrier per transponder” or in “multi-carriers per transponder” (FDM) configurations. In single carrier per transponder configurations, the transmission symbol rate R_s can be matched to given transponder bandwidth BW (at - 3 dB), to achieve the maximum transmission capacity compatible with the acceptable signal degradation due to transponder bandwidth limitations. To take into account possible thermal and ageing instabilities, reference can be made to the frequency response mask of the transponder. Group delay equalisation at the transmitter may be used to increase the transmission capacity or to reduce degradation.

In the multi-carrier FDM configuration, R_s can be matched to the frequency slot BS allocated to the service by the frequency plan, to optimise the transmission capacity while keeping the mutual interference between adjacent carriers at an acceptable level.

Figure G1.1 gives examples of the useful bit rate capacity R_u achievable by the System versus the LDPC code rate, assuming unit symbol rate R_s . The symbol rate R_s corresponds to the -3dB bandwidth of the modulated signal. $R_s(1+\alpha)$ corresponds to the theoretical total signal bandwidth after the modulator. The figures refer to Constant Coding and Modulation, normal FECFRAME length (64800 bit), no PADDING field, no pilots (the pilots would reduce the efficiency by about 2,4%). Typical BW/ R_s or BS/ R_s ratios are $\eta=1+\alpha=1,35$ and 1,20, where α is the roll-off factor of the modulation. This choice allows to obtain a negligible E_s/N_0 degradation due to transponder bandwidth limitations, and also to adjacent channel interference on a linear channel. BW/ R_s factors $< 1+\alpha$ may also be adopted, but careful studies should be carried-out on a case-by-case basis to avoid unacceptable interference and distortion levels.

Figure G1.2 shows the required C/N (Carrier-to-Noise power ratio measured in a bandwidth equal to the symbol rate) versus the spectrum efficiency (useful bit-rate for unit symbol rate R_s), obtained by computer simulations on the AWGN channel (ideal demodulator, no phase noise). Before Nyquist filtering in the modulator, the peak-to-average power ratio is 0 dB for BPSK, QPSK and 8PSK, while it is 1.5 dB for 16APSK and 2 dB for 32APSK (figures to be confirmed). When DVB-S2 is transmitted by satellite, quasi-constant envelope modulations, such as BPSK, QPSK and 8PSK, are power efficient in single carrier per transponder configuration, since they can operate on transponders driven near saturation. 16APSK and 32APSK, which are inherently more sensitive to non-linear distortions and would require quasi-linear transponders (i.e., with larger Output-Back-Off, OBO) may be improved in terms of power efficiency by using non-linear compensation techniques in the up-link station.

The use of the narrower roll-off $\alpha=0,25$ and $\alpha=0,20$ may allow a transmission capacity increase but may also produce larger non-linear degradations by satellite for single carrier operation.

In the FDM configuration, the satellite transponder must be quasi-linear (i.e., with large Output-Back-Off, OBO) to avoid excessive intermodulation interference between signals.

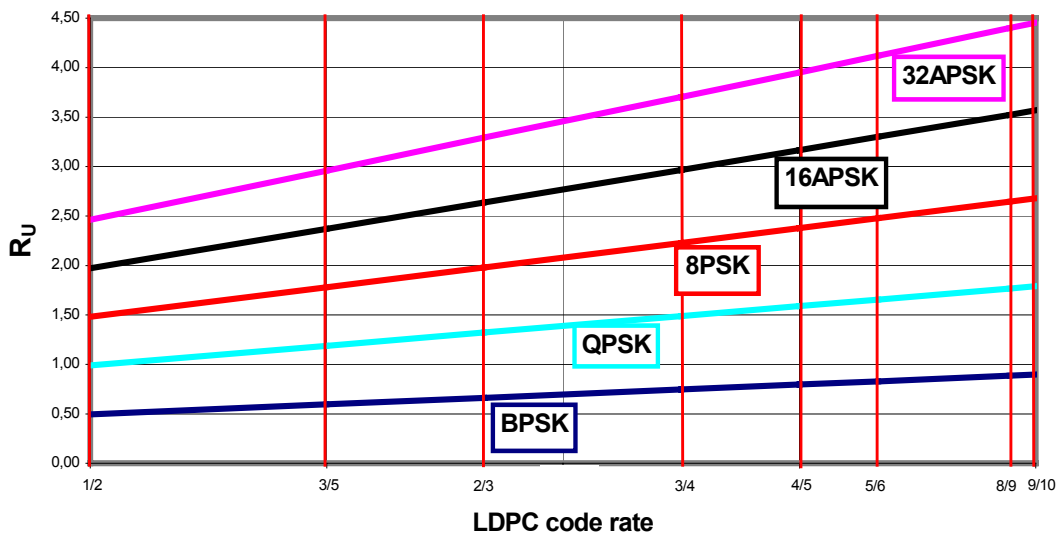


Figure G1.1: Examples of useful bit rates R_U versus LDPC code rate per unit symbol rate R_S

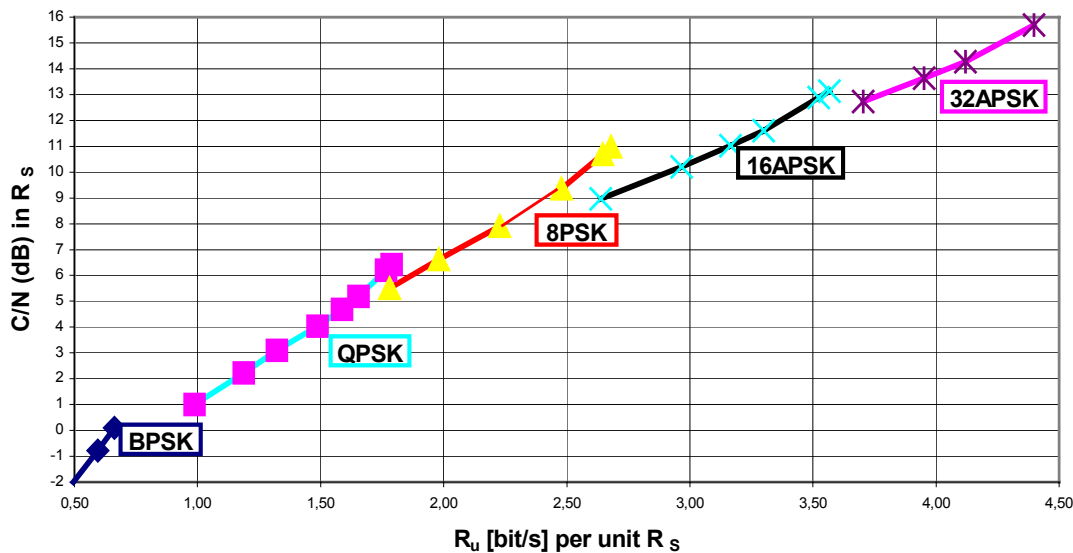


Figure G1.2: Required C/N versus spectrum efficiency, obtained by computer simulations on the AWGN channel (ideal demodulator) (C/N refers to average power)

Table G1.1 shows the C/N degradation measured by computer simulations using the satellite channel models given in **section G7** (non linearised TWTA) and **G8**. The following parameters have been simulated: $R_s=27.5$ Mbaud, roll-off=30% (not available in DVB-S2, but giving performance between roll-off 0.35 and 0.25), dynamic pre-distortion memory $M=5$ (for QPSK, 8PSK and 16APSK), $M=3$ (for 32APSK). C_{SAT} is the un-modulated carrier power at HPA saturation, OBO is the measured power ratio (dB) between the un-modulated carrier at saturation and the modulated carrier (after OMUX).

Table G1.1: C/N degradation [dB] on the satellite channel (simulation results)

Transmission Mode	C _{SAT} /N loss [dB] no predistortion without Phase Noise	C _{SAT} /N loss [dB] with dynamic predistortion without Phase Noise	C _{SAT} /N loss [dB] with Phase Noise
BPSK 2/3	NA	NA	NA
QPSK 1/2	0,7 (OBO=0,4)	0,9 (OBO=0,7)	NA
8PSK 2/3	1,1 (OBO=0,5)	0,9 (OBO=0,6)	NA
16APSK 3/4	3,6 (OBO=1,9)	1,8 (OBO=1,3)	NA
32APSK 4/5	6,8 (OBO=4,3)	2,9 (OBO=2,0)	NA

G2. Example of distribution of multiple MPEG multiplexes to Digital Terrestrial TV Transmitters (Multiple transport Streams, Constant Coding and Modulation)

The DVB-S2 system is suitable for the distribution of N MPEG multiplexes to digital terrestrial transmitters, using a single carrier per transponder configuration, thus optimising the power efficiency by saturating the satellite HPA (with the DVB-S system N carriers per transponder should be transmitted, requiring a large HPA OBO). For example, assuming the availability of a BW= 36 MHz transponder, a symbol rate of 30 Mbaud may be transmitted using $\alpha=0,20$. Thus to transmit two DTT MUXes at 24 Mbit/s each, a spectrum efficiency of 1.6 [bit/s/Hz] is required, corresponding to QPSK rate 5/6 (required C/N=5.2+0.7 dB + implementation margin). **Figure G1.1.** shows an example of possible configuration at the transmitting side.

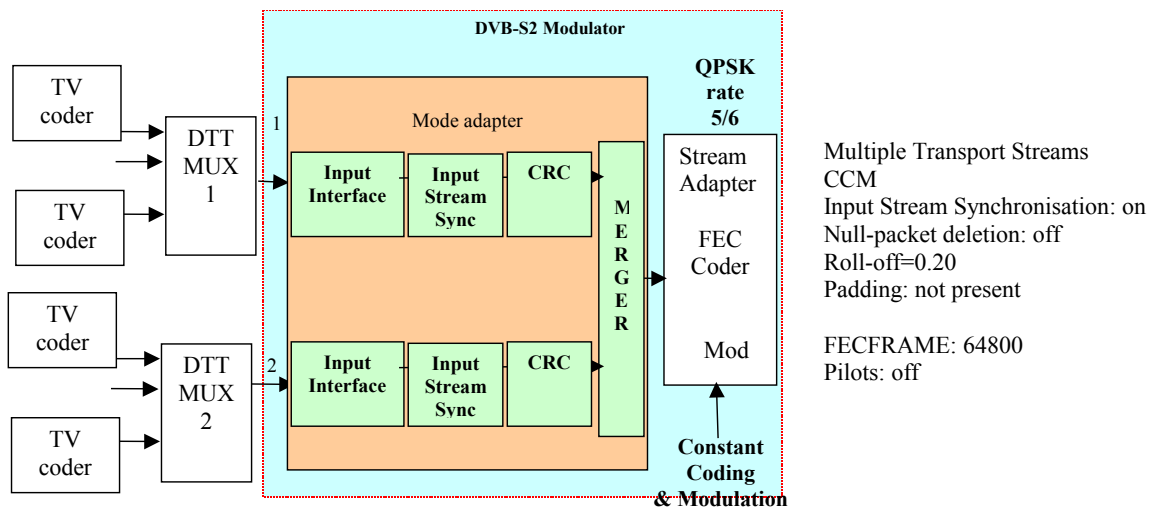


Figure G2.1: Example of DVB-S2 configuration for multiple DTT multiplexes distribution by satellite

G3. Example of SDTV and HDTV broadcasting with differentiated channel protection (VCM, Multiple Transport Streams with different protection levels)

The DVB-S2 system may deliver broadcasting services over multiple Transport Streams, providing differentiated error protection per Mux (VCM mode). A typical application is broadcasting of a highly protected Mux for SDTV, and of a less protected Mux for HDTV. It should be noted that the DVB-S2 system is unable to differentiate error protection within the same TS Mux. **Figure G3.1** shows an example configuration at the transmitting side. Assuming to transmit 27.5 Mbaud and to use 8PSK 3/4 and QPSK 2/3, 40 Mbit/s would be available for two HDTV programmes and 12 Mbit/s for two SDTV programmes. The difference in C/N requirements would be around 5 to 5.5 dB.

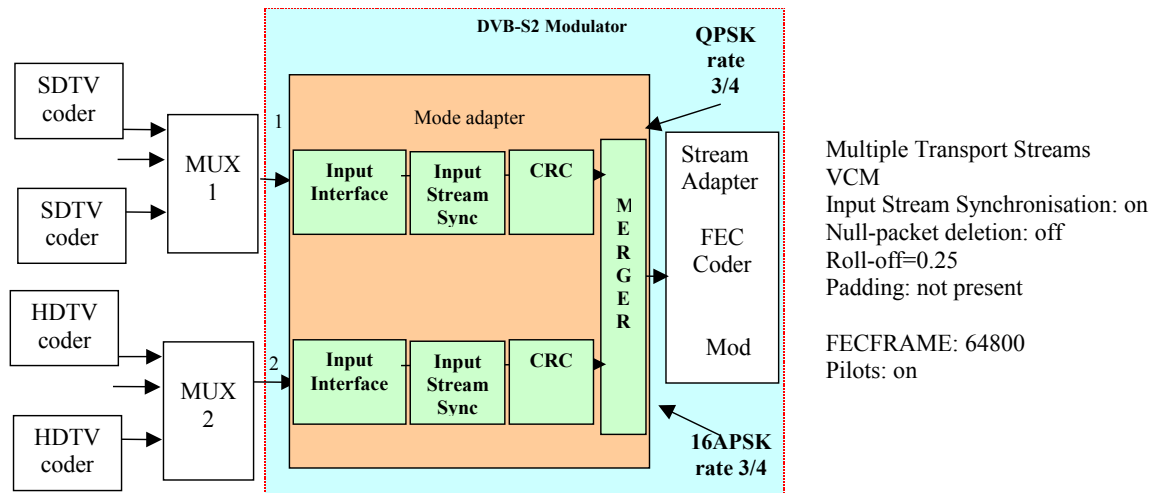


Figure G3.1: Example DVB-S2 configuration for multiple DTT multiplexes broadcasting using VCM

G4. DSNG Services using ACM (Single transport Stream, information rate varying in time)

In point-to-point ACM links, where a single TS is sent to a unique receiving station (e.g., DSNG), the TS packets protection must follow the C/N+I variations on the satellite channel in a given receiving location. When propagation conditions change (see **Figure G4.1**, yellow arrow), the PLFRAMES F_i switch from protection mode M_j to protection mode M_k to guarantee the service continuity .

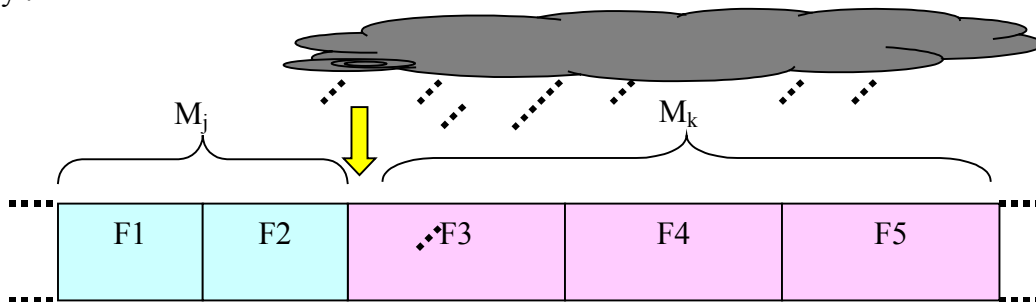


Figure G4.1: PLFRAMEs changing protection during a rain fading

The DVB-S2 system may operate as follows (see **Figure G4.2**, showing also example evaluation of the chain loop delays D1-D8):

1. the bit-rate control unit keeps the VBR source bit-rate (e.g. video encoder) at the maximum level compatible with the actual channel conditions C/N+I. In parallel, it sets the DVB-S2 modulator transmission mode via the “ACM Command” input port.
2. the TS bit-rate is set at the maximum level receivable at QeF in clear sky. The TS multiplexer adds null-packets to generate the constant bit-rate R_{TS} .
3. Null Packets (NP) are deleted in the Mode Adapter, so that the actual bit-rate on the channel corresponds to the source bit-rate. The deleted NPs are signalled in the NPD byte
4. the receiver re-inserts Null Packets exactly in the original position (by decoding DNP), and the Transport Stream clock is regenerated using the Input Stream Clock Reference (ISCR).

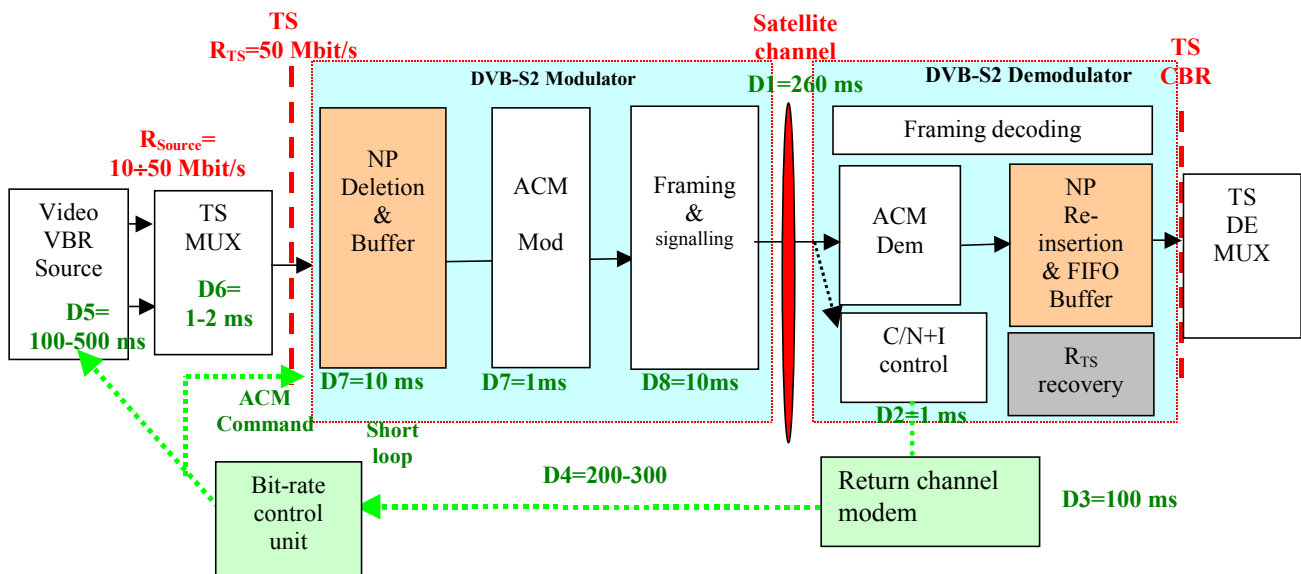


Figure G4.2: Single TS – uniform protection for long periods: transmission and receiving schemes

With reference to **Figure G4.2**, during a fast fading the bit rate control unit may impose a rate reduction first on the source encoder, and only after the command has been executed (e.g. after 100-500 ms), to the DVB-S2 modulator (via ACM Command). A drawback of this configuration is that the video encoder and MUX delays (D5 and D6 in **Figure G4.2**) are included in the control loop, with the risk of service outage under fast fading conditions. To overcome this additional delay the ACM Command can be instantly delivered also to the modulator, but to avoid packet losses large buffers have to be inserted in the DVB-S2 modulator and demodulator.

G5 IP Unicast Services (Non-uniform protection on a user-by-user basis)

Figure G5.1 shows a possible exchange of information (info request and info response) between the user, the satellite network operator and the information provider during an Internet navigation session by satellite (forward high capacity link).

These interactive data services may take advantage of:

- non-uniform error protection (ACM)
- differentiated service levels (priority in the delivery queues)

According to the negotiation between the user terminal and the “ACM routing manager“, an “ACM router” may in principle separate IP packets per user, per required error protection and per service level. The ACM routing manager ensures that the total capacity allocated to the IP services does not overload the available channel capacity ($\sum_i R_i / \eta_i = R_{IP}$; where R_i is the bit-rate and η_i is the

spectrum efficiency on the i-th protection level, R_{IP} is the portion of the symbol-rate allocated to IP services - $R_{IP} < R_S$). To fulfil this rule, when the total offered traffic becomes larger than the channel capacity, lower priority IP packets may be delayed (or even dropped) in favour of high priority packets. If the control-loop delays (including routing manager and ACM router) are too large to allow error free reception under fast-fading conditions, real time services (e.g. video/audio streaming) may be permanently allocated to a high protection branch, while lower priority services (e.g. best effort) may exploit the higher efficiency (i.e., lower cost) provided by ACM.

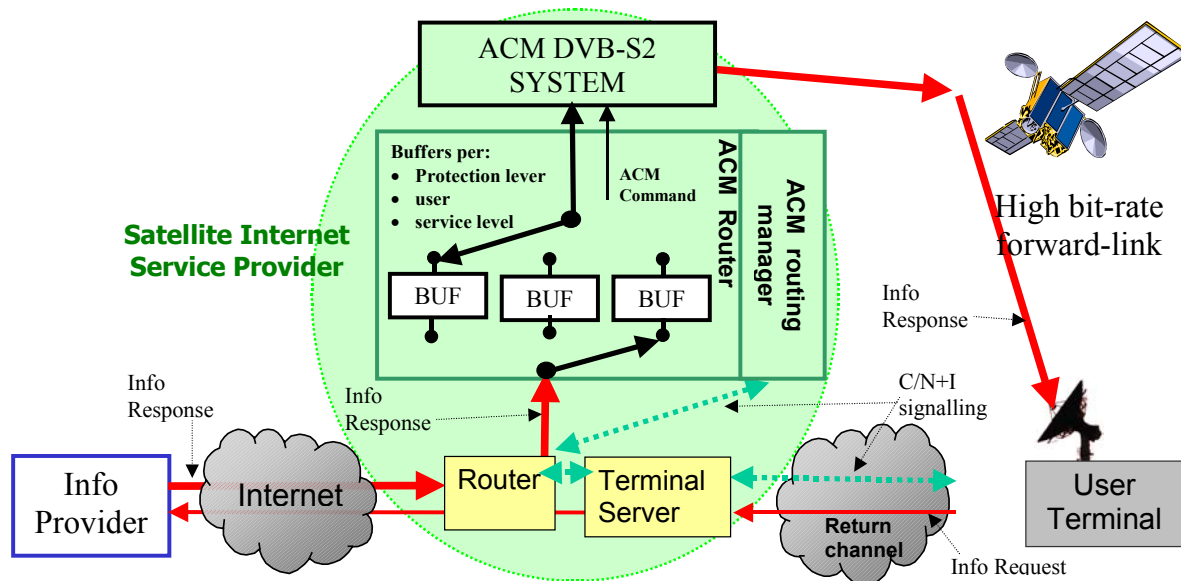


Figure G5.1. IP services using a DVB-S2 ACM link

The ACM router may interface with the DVB-S2 modulator:

- via a Single Generic Stream input and the ACM Command input. In this case the ACM router is independent from the DVB-S2 modulator, and may implement any routing policy. The DVB-S2 modulator immediately transmits the user data according to the ACM Command, therefore the loop delays may be minimised.
- via Multiple (Transport or Generic) Stream inputs, one per each active protection level (the ACM Command interface needs not be active). In this case the DVB-S2 Merger/Slicer partially covers the functionality of the ACM router

This latter case is represented in more detail in **Figure G5.2**. The ACM router splits the users' packets per service level (priority) and per required protection level, and sends them to the multiple DVB-S2 input interfaces, each stream being permanently associated to a given protection level. Therefore each input stream merges the traffic of all the users needing a specific protection level, and its useful bit-rate may (slowly) change in time according to the traffic characteristics. According to **Table D4.1**, the Merger/Slicer in **Figure G5.2** cyclically polls the input TS buffers, and conveys to the ACM modulator a block of users' data ready to fill (or partially fill) a PLFRAME. A timeout may be defined in order to avoid long delays in each merger/slicer buffer. dotted boxes in **Figure G5.2** address the specific case of IP services encapsulated in Transport Streams (Multi-Protocol Encapsulation - MPE), according to EN 301 192. In this case, K MPE gateways (GTW_i) are associated to K TS Multiplexers, to feed K DVB-S input streams (one per active protection level). Null-packet deletion, applied to each branch, reduces the transmitted bit-rate. The decoded TS, after null packets re-insertion, is a valid TS (the input stream synchroniser may optionally be activated). To fully exploit the potential ACM advantages, the additional control-loop delays introduced by the TS-specific equipment (Gateways, TS Muxes) should be minimised.

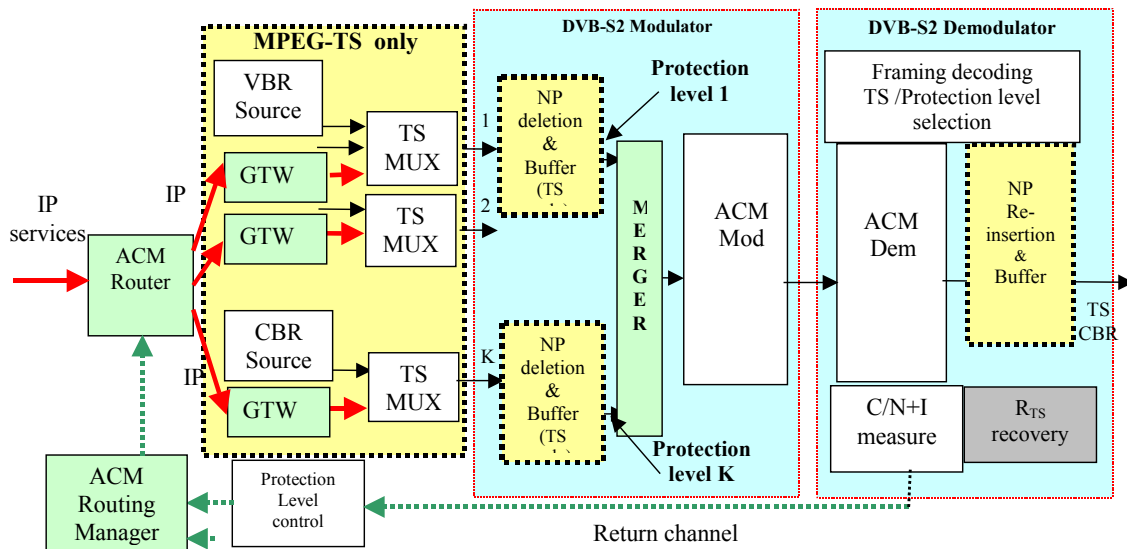


Figure G5.2: IP Unicasting and ACM: Multiple input streams – uniform protection per stream (for Generic input Streams, GTWs, TS Muxes and null-packet deletion are not required)

G6 Satellite Transponder Models for Simulations

For simulations, the “transparent” (i.e. non regenerative) satellite transponder model may be composed of an input filter (IMUX), a power amplifier (TWT or SSA) and an output filter (OMUX). Two amplifier models are here defined, the linearised TWTA (LTWTA) and the non-linearised TWTA. SSPAs have not been considered since they are less critical than TWTAs in terms of degradations.

The reference symbol rate with the specified IMUX/OMUX filter bandwidth is $R_s=27.5$ Mbaud.

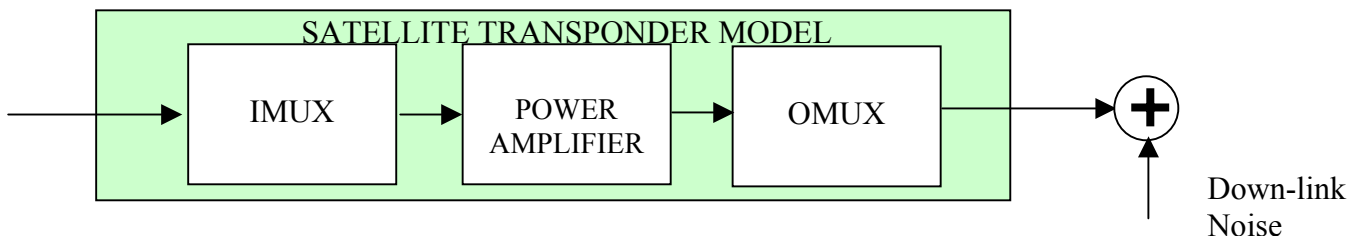


Figure G6.1 Satellite Transponder model

Figures G6.2 and G6.3 give the AM/AM and AM/PM TWTA characteristics.

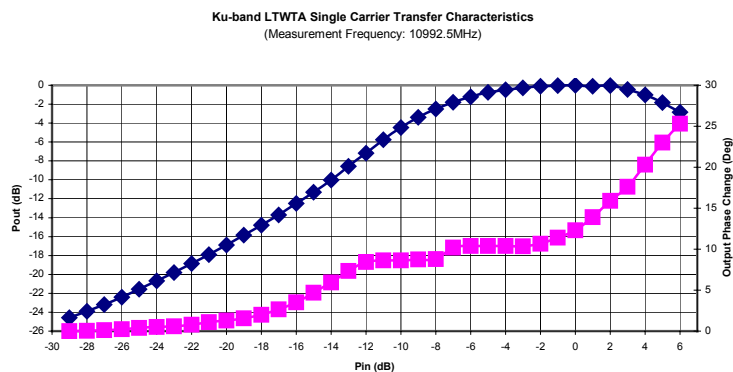


Figure G6.2 Linearised TWTA characteristic.

Ka-band TWTA - Single Carrier Transfer Characteristics

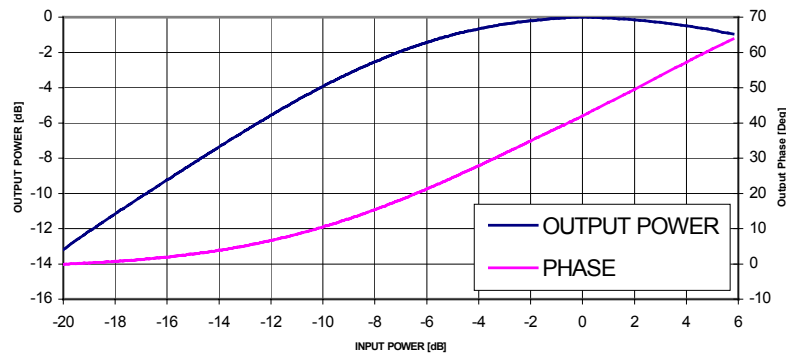


Figure G6.3 Non-Linearised TWTA characteristic.

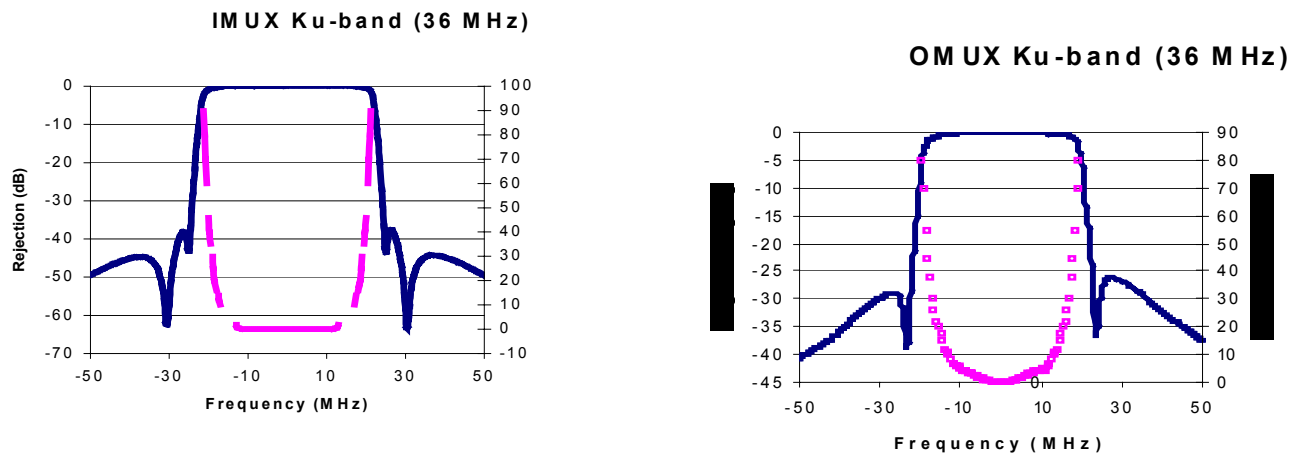


Figure G6.4 IMUX and OMUX characteristics.

Other transponder bandwidths BW [MHz] may be obtained by scaling the IMUX and OMUX characteristics:

$$R(f) = \text{Rejection} [f \times (BW/36)]$$

$$G(f) = [(36/BW)] \times \text{Group-delay} [f \times (BW/36)]$$

The band-centre insertion loss is not indicated, but should be included in C_{SAT} for link budget computation.

G7. Phase noise masks for simulations

The following phase noise masks for consumer reception systems may be used to evaluate the carrier recovery algorithms. The mask represents single side-band power spectral densities. The “aggregate” masks combine the phase noise contributions of the LNB and of the relevant Tuner. Other sources of phase noise within the chain (e.g. satellite transponder, up-link station,...) are usually negligible, and therefore the proposed masks may be considered as representative of the full chain.

Table G7.1: Aggregate Phase Noise masks for Simulation (in dBc/Hz)

frequency \Rightarrow	100 Hz	1 kHz	10 kHz	100 kHz	1 MHz	>10 MHz
Aggregate1 (typical)	-25	-50	-73	-93	-103	-114
Aggregate2 (critical)	-25	-50	-73	-85	-103	-114

Annex H (informative): Bibliography

For the purposes of this EN, the following informative references apply:

[1] R. De Gaudenzi, A. Guillen i Fabregas, A. Martinez Vicente, B. Ponticelli, “*APSK Coded Modulation Schemes for Nonlinear Satellite Channels with High Power and Spectral Efficiency*”, in the Proc. of the AIAA Satellite Communication Systems Conference 2002, Montreal, Canada, May 2002, Paper # 1861.

History

Document history		